

# MB836

Intel Atom 945GSE

5.25-inch SBC

## USER'S MANUAL

Version 1.0

Main Board Use in the  
FWA6104

**IBT Technologies Inc,**  
[www.ibt.ca](http://www.ibt.ca)

---

## Acknowledgments

Award is a registered trademark of Award Software International, Inc.

PS/2 is a trademark of International Business Machines Corporation.

**Intel®** and **Atom™** are registered trademark of Award Software International, Inc. registered trademark and trademark of Intel Corporation.

Microsoft Windows is a registered trademark of Microsoft Corporation.

Winbond is a registered trademark of Winbond Electronics Corporation.

All other product names or trademarks are properties of their respective owners.

---

# Table of Contents

<b>Introduction .....</b>	<b>1</b>
Product Description.....	1
Checklist.....	2
MB836 Specifications .....	3
Board Dimensions .....	4
<b>Installations .....</b>	<b>5</b>
Installing the Memory .....	6
Setting the Jumpers .....	7
Connectors on MB836 .....	10
Bypass and WDT .....	16
GPIO Function Definition.....	17
Digital I/O Sample Code .....	18
Watchdog Timer Configuration .....	21
<b>BIOS Setup.....</b>	<b>25</b>
<b>Drivers Installation .....</b>	<b>48</b>
Intel Chipset Software Installation Utility.....	49
VGA Drivers Installation .....	50
AC97 Codec Audio Driver Installation.....	51
LAN Drivers Installation.....	53
<b>Appendix .....</b>	<b>56</b>
A. I/O Port Address Map.....	56
B. Interrupt Request Lines (IRQ).....	57

---

This page is intentionally left blank.

# Introduction

## Product Description

The MB836 is a small footprint single board computer that is configured with the Intel Atom processor N270 at 1.6GHz, FSB533 and the Mobile Intel 945GSE Express Chipset with the ICH7M.

This 5.25-inch SBC provides greater flexibility for developers of embedded computing solutions. It is ideally suited for rugged and compact designs as in internet devices and applications in automation, industrial control, data acquisition, thin client and other embedded PC applications.

MB836 features the Intel's Graphics Media Accelerator 950 core, making it compatible with Windows Vista Premium, including VGA CRT,. One DDR2 SO-DIMM on board implements up to 2GB of system memory. 4 Ports Gigabit LAN is also available.

Advanced connectivity and expansion interfaces are provided by one IDE for CF socket, one high-speed SATA-II, two serial ports, four USB 2.0,

Model	WPCT2OODAOW G TPM	LAN Bypass	Watchdog Timer
MB836F	Yes	Yes	Yes
MB836	No	No	Yes

### Mb836 Features

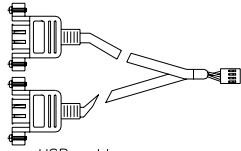
- Supports 4 Realtek Gigabit LAN ports
- Supports N270 at 1.6GHz, FSB533 processors
- DDR2 SO-DIMM x 1, up to 2GB
- Mini-PCI slot
- Compact Flash socket
- Low power and fanless, Aluminum enclosure
- Optional Hardware LAN Bypass function on ETH 0 & 1

## Checklist

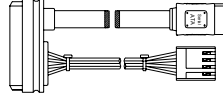
---

Your MB836 package should include the items listed below.

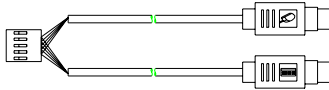
- The MB836 embedded board
- This User's Manual
- 1 CD containing chipset drivers and flash memory utility
- Cables are (shown below) optional.



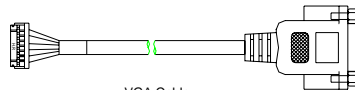
USB cable  
USB2-4C



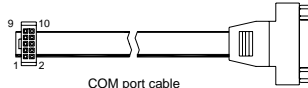
SATA & Power cable  
SATA-12



PS/2 KB/MS Cable  
PS2D



VGA Cable  
VGA12



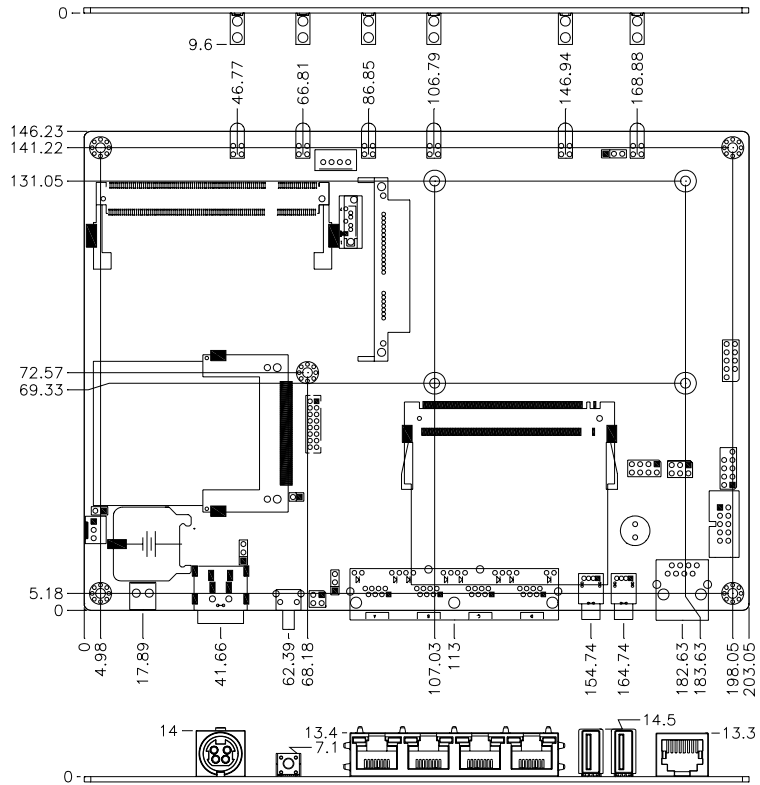
COM port cable  
PK1

## MB836 Specifications

### Intel Atom 5.25" Dsk Size SBC with Intel 945GSE Chipset

Project Name	MB836
Form Factor	5.25" SBC
Processor	Intel® Atom™ processor N270 (code name Diamondville SC processor) CPU Clock speed: 1.60GHz, FSB: 533MHz, L2 Cache: 512K TDP=2.5W
Memory	DDRII 533 SO-DIMM x1 (w/o ECC function), Max. 2GB
Chipset	MCH: INTEL 82945GSE 998-pin FCBGA ICH: Intel 82801GBM (ICH7M) 652-pin BGA
Green /APM	APM1.2
BIOS	Award BIOS: supports ACPI Function
Ethernet controller	Onboard 4X Realtek8111 PCIe GbE with RJ45 on the edge connector
Ethernet bypass	Default ETH0 ~ ETH1 with bypass , optional function (can control by GPIO and WDT)
TPM	Support TPM V1.2 with WINBOND WPCT200AA0WG
USB	Intel ICH7M build-in USB2.0 controller, support 4ports
SATA	Intel ICH7M build-in SATA controller, support 2ports
IDE	Intel ICH7M build-in PATA controller, support 1 IDE device for CF socket
LPC I/O	Windbond W83627EHG COM1 with RJ45 on edge connector for consol redirection COM2 with pitch 2.54mm 2*5 box header for LCM function Hardware monitor with thermal, voltage and FAN
Keyboard / mouse	PS/2 keyboard & mouse support with pitch 2.54mm pin header
Expansion slot	1X onboard MINI PCI slot on bottom side
Edge connector	1X 1*4 RJ45 connector with LED for GbE 2X USB stack connector 1X Console redirection via RJ45 without LED 1X 4P Power mini din connector 6X 1*2 LED for LAN & system status
	1X 3P FAN header with Smart FAN function 2X SATA controller with 2XSATA onboard 7-pin shrouded vertical connector 1X USB2.0 pitch 2.54mm 2*4 pin header 1X COM2 ports pitch 2.54mm 2*5 box header 1X PS/2 Key/Mouse pitch 2.54mm 2*5 pin header 1X CF socket on bottom side 1X 4P power connector (floppy type)
	From Left to Right 1*2 LED: Top for Power status Bottom for HDD status 1*2 LED: For GPO controller 4X 1*2 LED: Top for LAN Speed status 10M=no color / 100M=green / 1000M=orange Bottom for LAN Link/Active status
	DC+12V, AT mode
	Yes (256 segments: 0, 1, 2,...., 255 sec/min)

# Board Dimensions





---

## Installations

This section provides information on how to use the jumpers and connectors on the MB836 in order to set up a workable system. The topics covered are:

Installing the Memory .....	6
Setting the Jumpers.....	7
Connectors on MB836.....	10
Bypass and WDT.....	16
GPIO Function Definition .....	17
Digital I/O Sample Code .....	18

## Installing the Memory

---

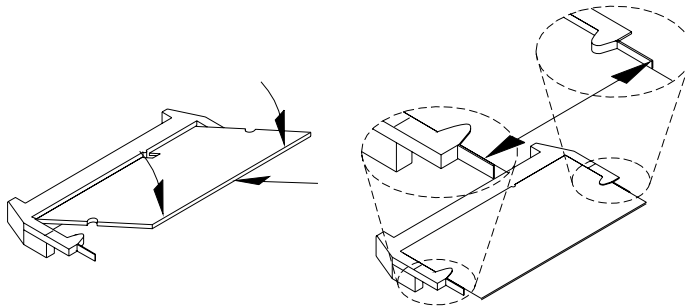
---

The MB836 board supports one DDR2 memory socket that can support up to 1GB memory, DDR2 400/533 (w/o ECC function).

### Installing and Removing Memory Modules

To install the DDR2 modules, locate the memory slot on the board and perform the following steps:

1. Hold the DDR2 module so that the key of the DDR2 module aligns with that on the memory slot. Insert the module into the socket at a slight angle (approximately 30 degrees). Note that the socket and module are both keyed, which means that the module can be installed only in one direction.
2. To seat the memory module into the socket, apply firm and even pressure to each end of the module until you feel it slip down into the socket.
3. With the module properly seated in the socket, rotate the module downward. Continue pressing downward until the clips at each end lock into position.
4. To remove the DDR2 module, press the clips with both hands.



---

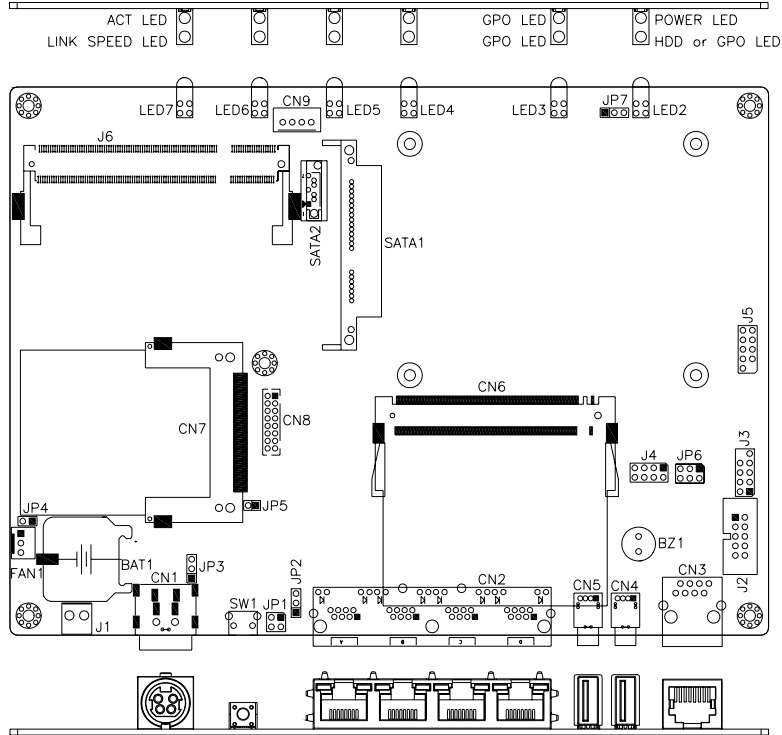
## Setting the Jumpers

---

Jumpers are used on MB836 to select various settings and features according to your needs and applications. Contact your supplier if you have doubts about the best configuration for your needs. The following lists the connectors on MB836 and their respective functions.



Jumper Locations on MB836 .....	7
JP2: LAN Bypass Setting .....	9
JP3: Clear CMOS Setting .....	9
JP7: HDD or GPO LED1 Selection.....	9

**Jumper Locations on MB836**

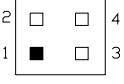


Jumpers on MB836.....	Page
JP2: LAN Bypass Setting .....	9
JP3: Clear CMOS Setting .....	9
JP7: HDD or GPO LED1 Selection.....	9

**JP2: LAN Bypass Setting**

JP2	ATX Power
	LAN Bypass By Watchdog
	LAN Bypass By GPO

*Remarks: To use this the JP2 jumper for bypass setting, the JP1 jumper should be open, as shown below.*



JP1	Setting
	1-2, 3-4 Open
	1-2, 3-4 Short

**Settings:**



*JP1: 1-2, 3-4 Open - System will Bypass LAN upon the timeout of WDT timer or a GPIO\_14.*

*JP1: 1-2, 3-4 Short - System will reboot upon the timeout of WDT timer.*

**JP3: Clear CMOS Setting**

JP3	Setting
	Normal
	Clear CMOS

**JP7: HDD or GPO LED2 Selection**

JP7	ATX Power
	HDD LED
	GPO LED2

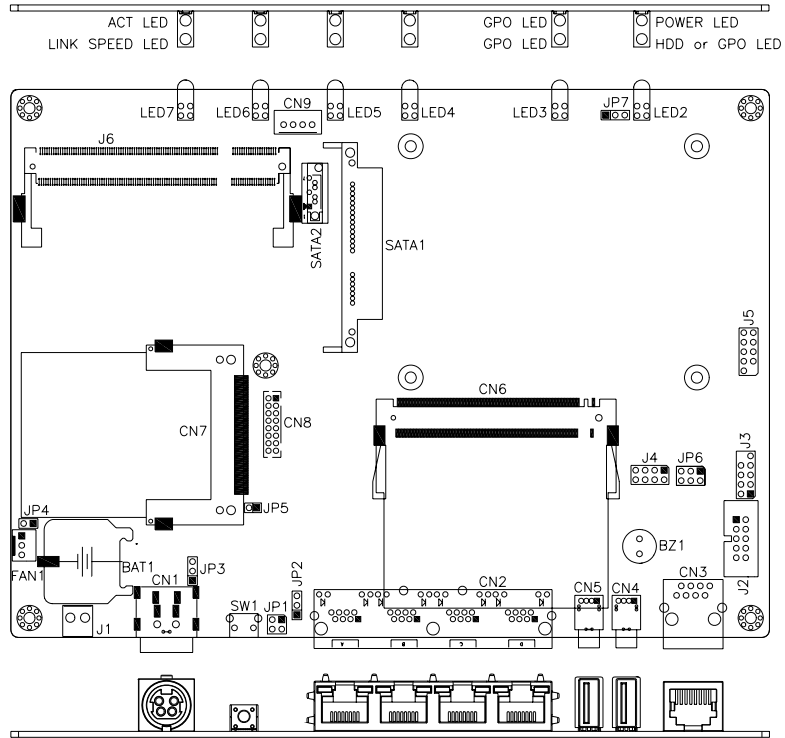
---

## Connectors on MB836

---

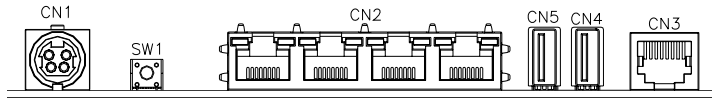
Connector Locations on MB836.....	11
CN1: DC Power Jack (+12V only).....	12
CN2: RJ45 LAN Connectors .....	12
CN3: Console port .....	12
The console port is an RJ45 RS-232 serial port.....	12
CN4, CN5: USB0/1 Ports .....	12
CN7: CF Socket.....	12
CN8: VGA Connector (DF11 Connector).....	13
CN9: HDD Power Connector .....	13
J2: COM2 Serial Port.....	13
J3: PS/2 Keyboard and Mouse Connector .....	13
JP4: Reset Switch Pin Header.....	14
JP6: GPO Control .....	14
SW1: GPI Switch Button Control.....	14
LED2: POWER & STATE LED .....	14
LED3: STATE LED .....	14
LED4, LED5, LED6, LED7: RJ45 LAN Link, Active LED.....	14
SATA1: SATA HDD Dock .....	15
SATA2: SATA Connector.....	15

**Connector Locations on MB836**

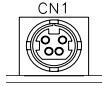


Connector Locations on MB836

CN1: DC Power Jack (+12V only) .....	12
CN2: RJ45 LAN Connectors .....	12
CN3: Console port.....	12
The console port is an RJ45 RS-232 serial port.....	12
CN4, CN5: USB0/1 Ports .....	12
CN7: CF Socket .....	12
CN8: VGA Connector (DF11 Connector) .....	13
CN9: HDD Power Connector.....	13
J2: COM2 Serial Port .....	13
J3: PS/2 Keyboard and Mouse Connector .....	13
JP4: Reset Switch Pin Header .....	14
JP6: GPO Control.....	14
SW1: GPI Switch Button Control.....	14
LED2: POWER & STATE LED.....	14
LED3: STATE LED.....	14
LED4, LED5, LED6, LED7: RJ45 LAN Link, Active LED.....	14
SATA1: SATA HDD Dock .....	15
SATA2: SATA Connector .....	15



**CN1: DC Power Jack (+12V only)**



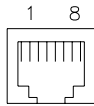
Pin #	Signal Name
1	+12V
2	+12V
3	GND
4	GND
5	GND

**CN2: RJ45 LAN Connectors**

These four LAN (LAN1/2/3/4) connectors are used in conjunction with the four Realtek Gigabit Ethernet controllers on the board.

**CN3: Console port**

The console port is an RJ45 RS-232 serial port.



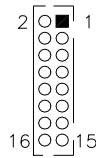
Pin #	Signal Name
1	RTS
2	DTR
3	TXD
4	GND
5	DCD
6	RXD
7	DSR
8	CTS

**CN4, CN5: USB0/1 Ports**

**CN7: CF Socket**



**CN8: VGA Connector (DF11 Connector)**



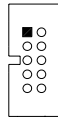
Signal Name	Pin	Pin	Signal Name
+5V	2	1	Red
Ground	4	3	Green
N.C	6	5	Blue
DDCDATA	8	7	N.C
H_SYNC	10	9	GND
V_SYNC	12	11	GND
DDCCLK	14	13	GND
N.C.	16	15	GND

**CN9: HDD Power Connector**



Pin #	Signal Name
1	+5V
2	Ground
3	Ground
4	+12V

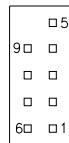
**J2: COM2 Serial Port**



Pin #	Signal Name
1	DCD, Data carrier detect
2	RXD, Receive data
3	TXD, Transmit data
4	DTR, Data terminal ready
5	Ground
6	DSR, Data set ready
7	RTS, Request to send
8	CTS, Clear to send
9	RI, Ring indicator

**J3: PS/2 Keyboard and Mouse Connector**

J3, a 10-pin header, has functions for both keyboard and mouse.



Signal Name	Pin #	Pin #	Signal Name
N.C.	10	5	N.C.
KB clock	9	4	Mouse clock
KB data	8	3	Mouse data
Vcc	7	2	Vcc
Ground	6	1	Ground

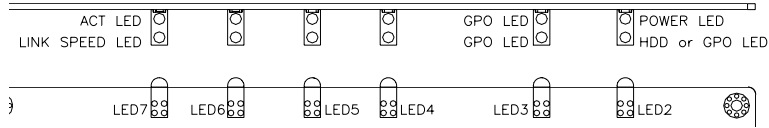
**JP4: Reset Switch Pin Header**

**JP6: GPO Control**



Signal Name	Pin #	Pin #	Signal Name
GPIO_11	6	3	GPIO_16
GPIO_15	5	2	GPIO_17
Vcc	4	1	Ground

SW1: GPI Switch Button (GPIO\_10)



**LED2: POWER & STATE LED**

Upper LED: HDD or GPIO\_11 LED (JP7 Setting)  
 Down LED: PWROK LED

**LED3: STATE LED**

Upper LED: GPIO\_12 LED  
 Down LED: GPIO\_13 LED

**LED4, LED5, LED6, LED7: RJ45 LAN Link, Active LED**

Upper LED: LINK SPEED LED  
 Down LED: Active LED

**SATA1: SATA HDD Dock**

The SATA HDD dock combines a SATA power connector and a SATA interface connector.

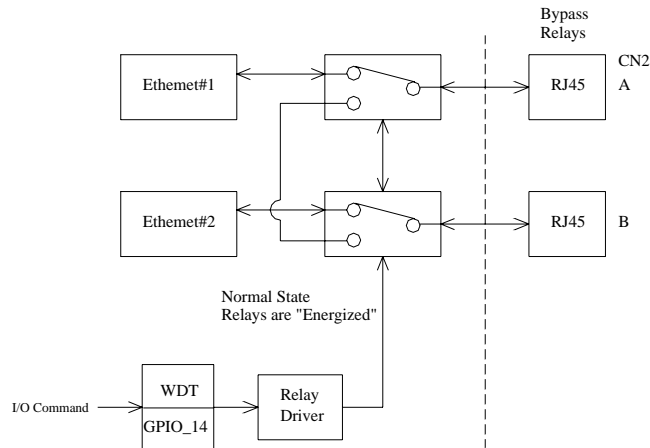
Signal Name	Pin #	Pin #	Signal Name
GND	S1	P1	V3.3
A+	S2	P2	V3.3
A-	S3	P3	V3.3
GND	S4	P4	GND
B+	S5	P5	GND
B-	S6	P6	GND
GND	S7	P7	GND
		P8	V5
		P9	V5
		P10	V5
		P11	Reserve
		P12	GND

**SATA2: SATA Connector**

## Bypass and WDT

The bypass function is used to link (or short) two independent Ethernet ports when user's application software halt or when power is off.

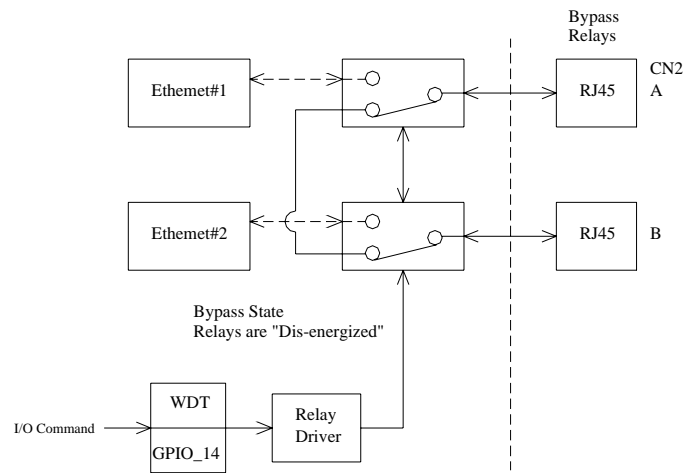
### Block Diagram:



### Communication States:

There are two communications states for the bypass function: (1) Normal State, (2) Bypass State. A watchdog timer (WDT) or a GPIO\_14 are used to control and switch the communication between the two states.

The block diagram in the section above shows the Normal State, where the two Ethernet ports work independently. The following diagram shows the Bypass State, where the two Ethernet ports are bypassed together.



## GPIO Function Definition

---

The MB836 has defined some of the GPI function to satisfy various requirements. Here are the definitions for the GPIO function:

- GPI\_10: button for customer defined (SW1)
- GPIO\_11~GPIO\_13: Status LED (LED2,LED3)
- GPIO\_14: Ethernet bypass function
- GPI\_15~GPIO\_17: Customer defined (JP6)

---

## Digital I/O Sample Code

---

```
Filename: W627hf.h
//=====
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//=====
#ifndef __W627HF_H
#define __W627HF_H                1
//=====
#define W627_IOWBASE                0x4E
//=====
#define W627HF_INDEX_PORT    (W627_IOWBASE+0)
#define W627HF_DATA_PORT    (W627_IOWBASE+1)
//=====
#define W627HF_REG_LD        0x07
//=====
#define W627HF_UNLOCK        0x87
#define W627HF_LOCK          0xAA
//=====
void Set_W627HF_LD( unsigned char);
void Set_W627HF_Reg( unsigned char, unsigned char);
unsigned char Get_W627HF_Reg( unsigned char);
//=====
#endif    // __W627HF_H
```

```
Filename: W627hf.cpp
//=====
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//=====
#include "W627HF.H"
#include <dos.h>
//=====
void Unlock_W627HF (void);
void Lock_W627HF (void);
//=====
void Unlock_W627HF (void)
{
    outportb(W627HF_INDEX_PORT, W627HF_UNLOCK);
    outportb(W627HF_INDEX_PORT, W627HF_UNLOCK);
}
//=====
void Lock_W627HF (void)
{
    outportb(W627HF_INDEX_PORT, W627HF_LOCK);
}
//=====
void Set_W627HF_LD( unsigned char LD)
{
    Unlock_W627HF();
    outportb(W627HF_INDEX_PORT, W627HF_REG_LD);
    outportb(W627HF_DATA_PORT, LD);
    Lock_W627HF();
}
//=====
void Set_W627HF_Reg( unsigned char REG, unsigned char DATA)
{
    Unlock_W627HF();
    outportb(W627HF_INDEX_PORT, REG);
    outportb(W627HF_DATA_PORT, DATA);
    Lock_W627HF();
}
//=====
unsigned char Get_W627HF_Reg( unsigned char REG)
{
    unsigned char Result;
    Unlock_W627HF();
    outportb(W627HF_INDEX_PORT, REG);
    Result = inportb(W627HF_DATA_PORT);
    Lock_W627HF();
    return Result;
}
//=====
File of the Main.cpp
```

```

//=====
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//=====
#include <dos.h>
#include <conio.h>
#include <stdio.h>
#include <stdlib.h>
#include "W627HF.H"
//=====
void ClrKbBuf(void);
int main (int argc, char *argv[]);
//=====
int main (int argc, char *argv[])
{
    unsigned char ucDO = 0;           //data for digital output
    unsigned char ucDI;              //data for digital input
    unsigned char ucBuf;

    Set_W627HF_LD( 0x07);            //switch to logic device 7

    Set_W627HF_Reg(0xF1, 0x00);      //clear
    ucDI = Get_W627HF_Reg(0xF1) & 0x0F;

    ClrKbBuf();
    while(1)
    {
        ucDO++;
        Set_W627HF_Reg(0xF1, ((ucDO & 0x0F) << 4));
        ucBuf = Get_W627HF_Reg(0xF1) & 0x0F;
        if (ucBuf != ucDI)
        {
            ucDI = ucBuf;
            printf("Digital I/O Input Changed. Current Data is 0x%X\n",ucDI);
        }

        if (kbhit())
        {
            getch();
            break;
        }
        delay(500);
    }
    return 0;
}
//=====
void ClrKbBuf(void)
{
    while(kbhit())
    {   getch();   }
}
//-----

```



## Watchdog Timer Configuration

The WDT is used to generate a variety of output signals after a user programmable count. The WDT is suitable for use in the prevention of system lock-up, such as when software becomes trapped in a deadlock. Under these sorts of circumstances, the timer will count to zero and the selected outputs will be driven. Under normal circumstance, the user will restart the WDT at regular intervals before the timer counts to zero.

### SAMPLE CODE:

```

=====
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
=====
#include <stdio.h>
#include <stdlib.h>
#include "W627EHF.H"
=====
int main (int argc, char *argv[]);
void copyright(void);
void EnableWDT(int);
void DisableWDT(void);
=====
int main (int argc, char *argv[])
{
    unsigned char bBuf;
    unsigned char bTime;
    char **endptr;

    copyright();

    if (argc != 2)
    {
        printf(" Parameter incorrect!!\n");
        return 1;
    }

    if (!init_W627EHF() == 0)
    {
        printf(" Winbond 83627HF is not detected, program abort.\n");
        return 1;
    }
    bTime = strtol (argv[1], endptr, 10);
    printf("System will reset after %d seconds\n", bTime);

    EnableWDT(bTime);

    return 0;
}
=====

```

```
void copyright(void)
{
    printf("\n===== Winbond 83627EHF Watch Timer Tester (AUTO DETECT) =====\n")
        "      Usage : W627E_WD reset_time\n"
        "      Ex : W627E_WD 3 => reset system after 3 second\n"
        "      W627E_WD 0 => disable watch dog timer\n");
}
//=====
void EnableWDT(int interval)
{
    unsigned char bBuf;

    bBuf = Get_W627EHF_Reg( 0x2D);
    bBuf &= (!0x01);
    Set_W627EHF_Reg( 0x2D, bBuf);           //Enable WDTO

    Set_W627EHF_LD( 0x08);                 //switch to logic device 8
    Set_W627EHF_Reg( 0x30, 0x01);         //enable timer

    bBuf = Get_W627EHF_Reg( 0xF5);
    bBuf &= (!0x08);
    Set_W627EHF_Reg( 0xF5, bBuf);         //count mode is second

    Set_W627EHF_Reg( 0xF6, interval);     //set timer
}
//=====
void DisableWDT(void)
{
    Set_W627EHF_LD(0x08);                 //switch to logic device 8
    Set_W627EHF_Reg(0xF6, 0x00);         //clear watchdog timer
    Set_W627EHF_Reg(0x30, 0x00);         //watchdog disabled
}
//=====
```

```

=====
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
=====
#include "W627EHF.H"
#include <dos.h>
=====
unsigned int W627EHF_BASE;
void Unlock_W627EHF (void);
void Lock_W627EHF (void);
=====
unsigned int Init_W627EHF(void)
{
    unsigned int result;
    unsigned char ucDid;

    W627EHF_BASE = 0x2E;
    result = W627EHF_BASE;

    ucDid = Get_W627EHF_Reg(0x20);
    if (ucDid == 0x88)
    {
        goto Init_Finish;
    }

    W627EHF_BASE = 0x4E;
    result = W627EHF_BASE;
    ucDid = Get_W627EHF_Reg(0x20);
    if (ucDid == 0x88)
    {
        goto Init_Finish;
    }

    W627EHF_BASE = 0x00;
    result = W627EHF_BASE;

Init_Finish:
    return (result);
}
=====
void Unlock_W627EHF (void)
{
    outportb(W627EHF_INDEX_PORT, W627EHF_UNLOCK);
    outportb(W627EHF_INDEX_PORT, W627EHF_UNLOCK);
}
=====
void Lock_W627EHF (void)
{
    outportb(W627EHF_INDEX_PORT, W627EHF_LOCK);
}
=====
void Set_W627EHF_LD( unsigned char LD)
{
    Unlock_W627EHF();
    outportb(W627EHF_INDEX_PORT, W627EHF_REG_LD);
    outportb(W627EHF_DATA_PORT, LD);
    Lock_W627EHF();
}

```

```

=====
void Set_W627EHF_Reg( unsigned char REG, unsigned char DATA)
{
    Unlock_W627EHF();
    outportb(W627EHF_INDEX_PORT, REG);
    outportb(W627EHF_DATA_PORT, DATA);
    Lock_W627EHF();
}
=====
unsigned char Get_W627EHF_Reg(unsigned char REG)
{
    unsigned char Result;
    Unlock_W627EHF();
    outportb(W627EHF_INDEX_PORT, REG);
    Result = inportb(W627EHF_DATA_PORT);
    Lock_W627EHF();
    return Result;
}
=====

=====
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//=====
#ifndef __W627EHF_H
#define __W627EHF_H                1
//=====
#define W627EHF_INDEX_PORT        (W627EHF_BASE)
#define W627EHF_DATA_PORT        (W627EHF_BASE+1)
//=====
#define W627EHF_REG_LD            0x07
//=====
#define W627EHF_UNLOCK            0x87
#define W627EHF_LOCK              0xAA
//=====
unsigned int Init_W627EHF(void);
void Set_W627EHF_LD( unsigned char);
void Set_W627EHF_Reg( unsigned char, unsigned char);
unsigned char Get_W627EHF_Reg( unsigned char);
//=====
#endif __W627EHF_H

```

---

## BIOS Setup

This chapter describes the different settings available in the Award BIOS that comes with the board. The topics covered in this chapter are as follows:

BIOS Introduction .....	26
BIOS Setup .....	26
Standard CMOS Setup .....	28
Advanced BIOS Features .....	31
Advanced Chipset Features .....	34
Integrated Peripherals .....	37
Power Management Setup .....	41
PNP/PCI Configurations .....	44
PC Health Status .....	45
Frequency/Voltage Control .....	46
Load Fail-Safe Defaults .....	47
Load Optimized Defaults .....	47
Set Supervisor/User Password .....	47
Save & Exit Setup .....	47
Exit Without Saving .....	47

## BIOS Introduction

The Award BIOS (Basic Input/Output System) installed in your computer system's ROM supports Intel processors. The BIOS provides critical low-level support for a standard device such as disk drives, serial ports and parallel ports. It also adds virus and password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

## BIOS Setup

The Award BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn on the computer, the Award BIOS is immediately activated. Pressing the <Del> key immediately allows you to enter the Setup utility. If you are a little bit late pressing the <Del> key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup. If you still wish to enter Setup, restart the system by pressing the "Reset" button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system Off and back On again. The following message will appear on the screen:

Press <DEL> to Enter Setup

In general, you press the arrow keys to highlight items, <Enter> to select, the <PgUp> and <PgDn> keys to change entries, <F1> for help and <Esc> to quit.

When you enter the Setup utility, the Main Menu screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.

Phoenix - AwardBIOS CMOS Setup Utility	
Standard CMOS Features	Frequency/Voltage Control
Advanced BIOS Features	Load Fail-Safe Defaults
Advanced Chipset Features	Load Optimized Defaults
Integrated Peripherals	Set Supervisor Password
Power Management Setup	Set User Password
PnP/PCI Configurations	Save & Exit Setup
PC Health Status	Exit Without Saving
ESC : Quit	↑ ↓ → ← : Select Item
F10 : Save & Exit Setup	
Time, Date, Hard Disk Type...	

The section below the setup items of the Main Menu displays the control keys for this menu. At the bottom of the Main Menu just below the control keys section, there is another section, which displays information on the currently highlighted item in the list.

**Note:** *If the system cannot boot after making and saving system changes with Setup, the Award BIOS supports an override to the CMOS settings that resets your system to its default.*

**Warning:** *It is strongly recommended that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both Award and your system manufacturer to provide the absolute maximum performance and reliability. Changing the defaults could cause the system to become unstable and crash in some cases.*

### Standard CMOS Setup

“Standard CMOS Setup” choice allows you to record some basic hardware configurations in your computer system and set the system clock and error handling. If the board is already installed in a working system, you will not need to select this option. You will need to run the Standard CMOS option, however, if you change your system hardware configurations, the onboard battery fails, or the configuration stored in the CMOS memory was lost or damaged.

Phoenix - AwardBIOS CMOS Setup Utility  
Standard CMOS Features

Date (mm:dd:yy)	Wed, Apr 28, 2004	Item Help
Time (hh:mm:ss)	00 : 00 : 00	Menu Level >
IDE Channel 0 Master	None	Change the day, month, Year and century
IDE Channel 0 Slave	None	
IDE Channel 1 Master	None	
IDE Channel 1 Slave	None	
Video	EGA/VGA	
Halt On	All, But Keyboard	
Base Memory	640K	
Extended Memory	129024K	
Total Memory	130048K	

At the bottom of the menu are the control keys for use on this menu. If you need any help in each item field, you can press the <F1> key. It will display the relevant information to help you. The memory display at the lower right-hand side of the menu is read-only. It will adjust automatically according to the memory changed. The following describes each item of this menu.

#### Date

The date format is:

**Day :** Sun to Sat  
**Month :** 1 to 12  
**Date :** 1 to 31  
**Year :** 1999 to 2099



---

To set the date, highlight the “Date” field and use the PageUp/PageDown or +/- keys to set the current time.

**Time**

The time format is: **Hour : 00 to 23**  
**Minute : 00 to 59**  
**Second : 00 to 59**

To set the time, highlight the “Time” field and use the <PgUp>/<PgDn> or +/- keys to set the current time.

**IDE Channel Master/Slave**

The onboard PCI IDE connector provides Primary and Secondary channels for connecting up to two IDE hard disks or other IDE devices.

Press <Enter> to configure the hard disk. The selections include Auto, Manual, and None. Select ‘Manual’ to define the drive information manually. You will be asked to enter the following items.

**CYLS :**                   Number of cylinders  
**HEAD :**                   Number of read/write heads  
**PRECOMP :**               Write precompensation  
**LANDING ZONE :**        Landing zone  
**SECTOR :**                Number of sectors

The Access Mode selections are as follows:

CHS   (HD < 528MB)  
LBA   (HD > 528MB and supports  
      Logical Block Addressing)  
Large (for MS-DOS only)  
Auto

**Remarks:** The main board supports two serial ATA ports and are represented in this setting as IDE Channel 0.

### Video

This field selects the type of video display card installed in your system.

You can choose the following video display cards:

EGA/VGA	For EGA, VGA, SEGA, SVGA or PGA monitor adapters. (default)
CGA 40	Power up in 40 column mode.
CGA 80	Power up in 80 column mode.
MONO	For Hercules or MDA adapters.

### Halt On

This field determines whether or not the system will halt if an error is detected during power up.

No errors	The system boot will not be halted for any error that may be detected.
All errors	Whenever the BIOS detects a non-fatal error, the system will stop and you will be prompted.
All, But Keyboard	The system boot will not be halted for a keyboard error; it will stop for all other errors
All, But Diskette	The system boot will not be halted for a disk error; it will stop for all other errors.
All, But Disk/Key	The system boot will not be halted for a keyboard or disk error; it will stop for all others.

## Advanced BIOS Features

This section allows you to configure and improve your system and allows you to set up some system features according to your preference.

Phoenix - AwardBIOS CMOS Setup Utility  
Advanced BIOS Features

		ITEM HELP
CPU Feature	Press Enter	
Hard Disk Boot Priority	Press Enter	
Virus Warning	Disabled	Menu Level >
CPU L1 and L2 Cache	Enabled	
CPU L3	Enabled	
Hyper-Threading Technology	Enabled	
Quick Power On Self Test	Enabled	
First Boot Device	Hard Disk	
Second Boot Device	CDROM	
Third Boot Device	USB-CDROM	
Boot Other Device	Enabled	
Swap Floppy Drive	Disabled	
Boot Up Floppy Seek	Disabled	
Boot Up NumLock Status	On	
Gate A20 Option	Fast	
Typematic Rate Setting	Disabled	
Typematic Rate (Chars/Sec)	6	
Typematic Delay (Msec)	250	
Security Option	Setup	
APIC Mode	Enabled	
MPS Version Control for OS	1.4	
OS Select For DRAM->64MB	Non-OS2	
Report No FDD For WIN 95	No	
Small Logo (EPA) Show	Disabled	

### CPU Feature

Press Enter to configure the settings relevant to CPU Feature.

### Hard Disk Boot Priority

With the field, there is the option to choose, aside from the hard disks connected, "Bootable add-in Cards" which refers to other external devices.

### Virus Warning

If this option is enabled, an alarm message will be displayed when trying to write on the boot sector or on the partition table on the disk, which is typical of the virus.

### CPU L1/L2/L3 Cache

Cache memory is additional memory that is faster than conventional DRAM (system memory). CPUs from 486-type on up contain internal cache memory, and most, but not all, modern PCs have additional (external) cache memory. When the CPU requests data, the system transfers the requested data from the main DRAM into cache memory, for even faster access by the CPU. These allow you to enable (speed up memory access) or disable the cache function.

### **Hyper-Threading Technology**

By default, this field is enabled.

### **Quick Power On Self Test**

When enabled, this field speeds up the Power On Self Test (POST) after the system is turned on. If it is set to *Enabled*, BIOS will skip some items.

### **First/Second/Third Boot Device**

These fields determine the drive that the system searches first for an operating system. The options available include *Floppy*, *LS120*, *Hard Disk*, *CDROM*, *ZIP100*, *USB-Floppy*, *USB-ZIP*, *USB-CDROM*, *LAN* and *Disable*.

### **Boot Other Device**

These fields allow the system to search for an OS from other devices other than the ones selected in the First/Second/Third Boot Device.

### **Boot Up NumLock Status**

This allows you to activate the NumLock function after you power up the system.

### **Gate A20 Option**

This field allows you to select how Gate A20 is worked. Gate A20 is a device used to address memory above 1 MB.

### **Typematic Rate Setting**

When disabled, continually holding down a key on your keyboard will generate only one instance. When enabled, you can set the two typematic controls listed next. By default, this field is set to *Disabled*.

### **Typematic Rate (Chars/Sec)**

When the typematic rate is enabled, the system registers repeated keystrokes speeds. Settings are from 6 to 30 characters per second.

### **Typematic Delay (Msec)**

When the typematic rate is enabled, this item allows you to set the time interval for displaying the first and second characters. By default, this item is set to *250msec*.

**Security Option**

This field allows you to limit access to the System and Setup. The default value is *Setup*. When you select *System*, the system prompts for the User Password every time you boot up. When you select *Setup*, the system always boots up and prompts for the Supervisor Password only when the Setup utility is called up.

**APIC Mode**

APIC stands for Advanced Programmable Interrupt Controller. The default setting is *Enabled*.

**MPS Version Control for OS**

This option specifies the MPS (Multiprocessor Specification) version for your operating system. MPS version 1.4 added extended configuration tables to improve support for multiple PCI bus configurations and improve future expandability. The default setting is *1.4*.

**OS Select for DRAM > 64MB**

This option allows the system to access greater than 64MB of DRAM memory when used with OS/2 that depends on certain BIOS calls to access memory. The default setting is *Non-OS/2*.

**Small Logo (EPA) Show**

The EPA logo appears at the right side of the monitor screen when the system is boot up.

## Advanced Chipset Features

This Setup menu controls the configuration of the chipset.

Phoenix - AwardBIOS CMOS Setup Utility  
Advanced Chipset Features

		ITEM HELP
DRAM Timing Selectable	By SPD	Menu Level >
CAS Latency Time	Auto	
DRAM RAS# to CAS# Delay	Auto	
DRAM RAS# Precharge	Auto	
Precharge delay (tRAS)	Auto	
System Memory Frequency	Auto	
SLP_S4# Assertion Width	4 to 5 Sec	
System BIOS Cacheable	Enabled	
Video BIOS Cacheable	Disabled	
Memory Hole at 15M-16M	Disabled	
PCI Express Root Port Func	Press Enter	
** VGA Setting **		
On-Chip Frame Buffer Size	8MB	
DVMT Mode	DVMT	
DVMT/FIXED memory Size	128MB	
SDVO Device Setting	DVI	
Boot Display	CRT	
Panel Scaling	Auto	
Panel Number	1024x768 18 bit SC	

### DRAM Timing Selectable

This option refers to the method by which the DRAM timing is selected. The default is **By SPD**.

### CAS Latency Time

You can configure CAS latency time in HCLKs as 2 or 2.5 or 3. The system board designer should set the values in this field, depending on the DRAM installed. Do not change the values in this field unless you change specifications of the installed DRAM or the installed CPU.

### DRAM RAS# to CAS# Delay

This option allows you to insert a delay between the RAS (Row Address Strobe) and CAS (Column Address Strobe) signals. This delay occurs when the SDRAM is written to, read from or refreshed. Reducing the delay improves the performance of the SDRAM.

---

**DRAM RAS# Precharge**

This option sets the number of cycles required for the RAS to accumulate its charge before the SDRAM refreshes. The default setting for the Active to Precharge Delay is *Auto*.

**Precharge Delay (tRAS)**

The default setting for the Precharge Delay is *Auto*.

**System Memory Frequency**

The default setting is *Auto*.

**SLP\_S4# Assertion Width**

The default setting is *1 to 2 Sec*.

**System BIOS Cacheable**

The setting of *Enabled* allows caching of the system BIOS ROM at F000h-FFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

**Video BIOS Cacheable**

The Setting *Enabled* allows caching of the video BIOS ROM at C0000h-F7FFFh, resulting in better video performance. However, if any program writes to this memory area, a system error may result.

**Memory Hole At 15M-16M**

In order to improve performance, certain space in memory can be reserved for ISA cards. This memory must be mapped into the memory space below 16 MB. The choices are *Enabled* and *Disabled*.

**On-Chip VGA Setting**

The fields under the On-Chip VGA Setting and their default settings are:

On-Chip Frame Buffer Size: 8MB  
DVMT Mode: DVMT  
DVMT/Fixed Memory Size: 128MB  
SDVO Device Setting: DVI  
Boot Display: CRT  
Panel Scaling: Auto  
Panel Number: 1024x768 18 bit SC

**Panel Scaling**

The default setting is *Auto*. The options available include *On* and *Off*.

**Panel Number**

These fields allow you to select the LCD Panel type. The default values for these ports are:

640x480	18bit SC
800x480	18bit SC
800x600	18bit SC
1024x768	18bit SC
1280x768	18bit SC
1280x800	18bit SC
1366x768	18bit SC



### Integrated Peripherals

This section sets configurations for your hard disk and other integrated peripherals. The first screen shows three main items for user to select. Once an item selected, a submenu appears. Details follow.

Phoenix - AwardBIOS CMOS Setup Utility  
Integrated Peripherals

OnChip IDE Device	Press Enter	ITEM HELP
Onboard Device	Press Enter	Menu Level >
SuperIO Device	Press Enter	
Watch Dog Timer Select	Disabled	

Phoenix - AwardBIOS CMOS Setup Utility  
**OnChip IDE Device**

IDE HDD Block Mode	Enabled	ITEM HELP
IDE DMA Transfer Access	Enabled	Menu Level >
On-chip Primary PCI IDE	Enabled	
IDE Primary Master PIO	Auto	
IDE Primary Slave PIO	Auto	
IDE Primary Master UDMA	Auto	
IDE Primary Slave UDMA	Auto	
On-Chip Secondary PCI IDE	Enabled	
IDE Secondary Master PIO	Auto	
IDE Secondary Slave PIO	Auto	
IDE Secondary Master UDMA	Auto	
IDE Secondary Slave UDMA	Auto	
*** On-Chip Serial ATA Setting ***		
On-Chip Serial ATA	Auto	
SATA PORT Speed Setting	Disabled	
PATA IDE Mode	Secondary	
SATA port	P0, P2 is Primary	
TPM Support	Disabled	
TPM Current Status	Enabled & Deactivate	
TPM Status	No Change	

Phoenix - AwardBIOS CMOS Setup Utility  
**Onboard Device**

USB Controller	Enabled	ITEM HELP
USB 2.0 Controller	Enabled	Menu Level >
USB Keyboard Support	Enabled	
USB Mouse Support	Enabled	
Azalia/AC97 Audio Select	Auto	

		ITEM HELP
POWER ON Function	BUTTON ONLY	
KB Power ON Password	Enter	
Hot Key power ON	Ctrl-F1	
Onboard Serial Port 1	3F8/IRQ4	
Onboard Serial Port 2	2F8/IRQ3	
Onboard Parallel Port Mode	378/IRQ7	
Parallel Port Mode	SPP	
EPP Mode Select	EPP1.7	
EPP Mode Use DMA	3	
PWRON After PWR-Fail	Off	
		Menu Level >

**IDE HDD Block Mode**

This field allows your hard disk controller to use the fast block mode to transfer data to and from your hard disk drive.

**IDE DMA Transfer Access**

By default, this field is enabled.

**On-chip Primary PCI IDE Enabled**

By default, this field is enabled.

**OnChip Primary/Secondary PCI IDE**

The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select *Enabled* to activate each channel separately.

**IDE Primary/Secondary Master/Slave PIO**

These fields allow your system hard disk controller to work faster. Rather than have the BIOS issue a series of commands that transfer to or from the disk drive, PIO (Programmed Input/Output) allows the BIOS to communicate with the controller and CPU directly.

The system supports five modes, numbered from 0 (default) to 4, which primarily differ in timing. When Auto is selected, the BIOS will select the best available mode.

**IDE Primary/Secondary Master/Slave UDMA**

These fields allow your system to improve disk I/O throughput to 33Mb/sec with the Ultra DMA/33 feature. The options are *Auto* and *Disabled*.

---

### On-Chip Serial ATA Setting

The fields under the SATA setting include:

SATA Mode	IDE; (other setting is AHCI)
On-Chip Serial ATA Auto	
PATA IDE Mode	Secondary
SATA port	P0, P2 is Primary

### USB Controller

The options for this field are *Enabled* and *Disabled*. By default, this field is set to *Enabled*.

### USB 2.0 Controller

The options for this field are *Enabled* and *Disabled*. By default, this field is set to *Enabled*. In order to use USB 2.0, necessary OS drivers must be installed first. *Please update your system to Windows 2000 SP4 or Windows XP SP2.*

### USB Keyboard Support

The options for this field are *Enabled* and *Disabled*.

### AC97 Audio Select

This field, by default, is set to *Auto*.

### Power ON Function

This field is related to how the system is powered on – such as with the use of conventional power button, keyboard or hot keys. The default is *BUTTON ONLY*.

### KB Power ON Password

This field allows users to set the password when keyboard power on is the mode of the Power ON function.

### Hot Key Power ON

This field sets certain keys, also known as hot keys, on the keyboard that can be used as a ‘switch’ to power on the system.

### Onboard Serial Port

These fields allow you to select the onboard serial ports and their addresses. The default values for these ports are:

Serial Port 1	3F8/IRQ4
Serial Port 2	2F8/IRQ3

**PWRON After PWR-Fail**

This field sets the system power status whether *on* or *off* when power returns to the system from a power failure situation.

## Power Management Setup

Phoenix - AwardBIOS CMOS Setup Utility  
Power Management Setup

		ITEM HELP
ACPI Function	Enabled	
ACPI Suspend	S1(POS)	
RUN VGABIOS if S3 Resume	Auto	Menu Level >
Power Management	User Define	
Video Off Method	DPMS	
Video Off In Suspend	Yes	
Suspend Type	Stop Grant	
Modem Use IRQ	3	
Suspend Mode	Disabled	
HDD Power Down	Disabled	
Soft-Off by PWR-BTTN	Instant-Off	
CPU THRM-Throttling	75%	
Wake-Up by PCI Card	Disabled	
Power On by Ring	Disabled	
Resume by Alarm	Disabled	
Date (of Month) Alarm	0	
Time (hh:mm:ss) Alarm	0 : 0 : 0	
** Reload Global Timer Events **		
Primary IDE 0	Disabled	
Primary IDE 1	Disabled	
Secondary IDE 0	Disabled	
Secondary IDE 1	Disabled	
FDD, COM, LPT Port	Disabled	
PCI PIRQ[A-D] #	Disabled	

### ACPI Function

Enable this function to support ACPI (Advance Configuration and Power Interface).

### ACPI Suspend

The default setting of the ACPI Suspend mode is *S1(POS)*.

### RUN VGABIOS if S3 Resume

The default setting of this field is *Auto*.

**Power Management**

This field allows you to select the type of power saving management modes. There are four selections for Power Management.

Min. Power Saving	Minimum power management
Max. Power Saving	Maximum power management.
User Define	Each of the ranges is from 1 min. to 1hr. Except for HDD Power Down which ranges from 1 min. to 15 min.

**Video Off Method**

This field defines the Video Off features. There are three options.

V/H SYNC + Blank	Default setting, blank the screen and turn off vertical and horizontal scanning.
DPMS	Allows BIOS to control the video display.
Blank Screen	Writes blanks to the video buffer.

**Video Off In Suspend**

When enabled, the video is off in suspend mode. The default setting is *Yes*.

**Suspend Type**

The default setting for the Suspend Type field is *Stop Grant*.

**Modem Use IRQ**

This field sets the IRQ used by the Modem. By default, the setting is *3*.

**Suspend Mode**

When enabled, and after the set time of system inactivity, all devices except the CPU will be shut off.

**HDD Power Down**

When enabled, and after the set time of system inactivity, the hard disk drive will be powered down while all other devices remain active.

**Soft-Off by PWRBTN**

This field defines the power-off mode when using an ATX power supply. The *Instant Off* mode allows powering off immediately upon pressing the power button. In the *Delay 4 Sec* mode, the system powers off when the power button is pressed for more than four seconds or enters the suspend mode when pressed for less than 4 seconds.

**CPU THRM-Throttling**

By default, this field is set to *75%*.

**Wake up by PCI Card**

By default, this field is disabled.

**Power On by Ring**

This field enables or disables the power on of the system through the modem connected to the serial port or LAN.

**Resume by Alarm**

This field enables or disables the resumption of the system operation. When enabled, the user is allowed to set the *Date* and *Time*.

**Reload Global Timer Events**

The HDD, FDD, COM, LPT Ports, and PCI PIRQ are I/O events that can prevent the system from entering a power saving mode or can awaken the system from such a mode. When an I/O device wants to gain the attention of the operating system, it signals this by causing an IRQ to occur. When the operating system is ready to respond to the request, it interrupts itself and performs the service.

### PNP/PCI Configurations

This option configures the PCI bus system. All PCI bus systems on the system use INT#, thus all installed PCI cards must be set to this value.

Phoenix - AwardBIOS CMOS Setup Utility  
PnP/PCI Configurations

Init Display First	PCI Slot	ITEM HELP  Menu Level  Select Yes if you are using a Plug and Play capable operating system Select No if you need the BIOS to configure non-boot devices
Reset Configuration Data	Disabled	
Resources Controlled By	Auto (ESCD)	
IRQ Resources	Press Enter	
PCI/VGA Palette Snoop	Disabled	
**PCI Express relative items**		
Maximum Payload Size	128	

#### Init Display First

The default setting is *Onboard*.

#### Reset Configuration Data

This field allows you to determine whether to reset the configuration data or not. The default value is *Disabled*.

#### Resources Controlled by

This PnP BIOS can configure all of the boot and compatible devices with the use of a PnP operating system such as Windows 95.

#### PCI/VGA Palette Snoop

Some non-standard VGA display cards may not show colors properly. This field allows you to set whether or not MPEG ISA/VESA VGA cards can work with PCI/VGA. When this field is enabled, a PCI/VGA can work with an MPEG ISA/VESA VGA card. When this field is disabled, a PCI/VGA cannot work with an MPEG ISA/VESA card.

#### Maximum Payload Size

The default setting of the PCI Express Maximum Payload Size is 128.



## PC Health Status

This section shows the parameters in determining the PC Health Status. These parameters include temperatures, fan speeds and voltages.

Phoenix - AwardBIOS CMOS Setup Utility  
PC Health Status

		ITEM HELP
Shutdown Temperature	Disabled	
CPU Warning Temperature	Disabled	
Current System Temp	45°C/113°F	Menu Level >
Current CPU Temp	45°C/113°F	
Fan1 Speed	5625	
Fan2 Speed	0	
Vcore(V)	1.02 V	
+12 V	1.32 V	
1.8V	1.8V	
+5V	5.25 V	
3.3V	3.37V	
VBAT (V)	3.21 V	
5VSB(V)	5.67 V	
** Smart FAN I **		
1 <sup>st</sup> Smart Fan Temperature	Disabled	
Fan1 Tolerance Value	5	
2 <sup>nd</sup> Smart Fan Temperature	Disabled	
Fan2 Tolerance Value	5	

### Shutdown Temperature

This field allows the user to set the temperature by which the system automatically shuts down once the threshold temperature is reached. This function can help prevent damage to the system that is caused by overheating.

### CPU Warning Temperature

This field allows the user to set the temperature so that when the temperature is reached, the system sounds a warning. This function can help prevent damage to the system that is caused by overheating.

### Temperatures/Voltages

These fields are the parameters of the hardware monitoring function feature of the board. The values are read-only values as monitored by the system and show the PC health status.

## Frequency/Voltage Control

This section shows the user how to configure the processor frequency.

Phoenix - AwardBIOS CMOS Setup Utility  
Frequency/Voltage Control

Auto Detect PCI Clk	Disabled	ITEM HELP
Spread Spectrum	Disabled	Menu Level >
CPU Host/SRC PCI Clock	Default	

### Auto Detect PCI Clk

This field enables or disables the auto detection of the PCI clock.

### Spread Spectrum Modulated

This field sets the value of the spread spectrum. The default setting is *Disabled*. This field is for CE testing use only.

### CPU Host / SRC PCI Clock

This field is set to *Default*.

**Load Fail-Safe Defaults**

This option allows you to load the troubleshooting default values permanently stored in the BIOS ROM. These default settings are non-optimal and disable all high-performance features.

**Load Optimized Defaults**

This option allows you to load the default values to your system configuration. These default settings are optimal and enable all high performance features.

**Set Supervisor Password**

These two options set the system password. Supervisor Password sets a password that will be used to protect the system and Setup utility. User Password sets a password that will be used exclusively on the system. To specify a password, highlight the type you want and press <Enter>. The Enter Password: message prompts on the screen. Type the password, up to eight characters in length, and press <Enter>. The system confirms your password by asking you to type it again. After setting a password, the screen automatically returns to the main screen.

To disable a password, just press the <Enter> key when you are prompted to enter the password. A message will confirm the password to be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

**Save & Exit Setup**

This option allows you to determine whether or not to accept the modifications. If you type "Y", you will quit the setup utility and save all changes into the CMOS memory. If you type "N", you will return to Setup utility.

**Exit Without Saving**

Select this option to exit the Setup utility without saving the changes you have made in this session. Typing "Y" will quit the Setup utility without saving the modifications. Typing "N" will return you to Setup utility.

## Drivers Installation

This section describes the installation procedures for software and drivers under the Windows 2000 and Windows XP. The software and drivers are included with the board. If you find the items missing, please contact the vendor where you made the purchase. The contents of this section include the following:

Intel Chipset Software Intallation Utility .....	49
VGA Drivers Installation .....	50
AC97 Codec Audio Driver Installation.....	51
LAN Drivers Installation.....	53

**IMPORTANT NOTE:**

After installing your Windows operating system (Windows 2000/ XP), you must install first the Intel Chipset Software Installation Utility before proceeding with the drivers installation.

---

## Intel Chipset Software Installation Utility

---

The Intel Chipset Drivers should be installed first before the software drivers to enable Plug & Play INF support for Intel chipset components. Follow the instructions below to complete the installation under Windows 2000/XP.

1. Insert the CD that comes with the board. Click **Intel** at the left side, then **Intel(R) I945GM/GME/GSE Chipset Drivers**.

2. Click **Intel(R) Chipset Software Installation Utility**.



3. When the welcome screen to the Intel(R) Chipset Software Installation Utility appears, click **Next** to continue.

4. Click **Yes** to accept the software license agreement and proceed with the installation process.

5. On Readme Information screen, click **Next** to continue the installation.

6. The Setup process is now complete. Click **Finish** to restart the computer and for changes to take effect.

---

## VGA Drivers Installation

---

To install the VGA drivers, follow the steps below to proceed with the installation.

1. Insert the CD that comes with the board. Click **Intel** at the left side, then **Intel(R) I945GM/GME/GSE Chipset Drivers**.
2. Click **Intel(R) I945GM/GME/GSE Chipset Family Graphics Driver**.



3. When the welcome screen of the Intel(R) Graphics Media Accelerator Driver appears, click **Next** to continue.
4. Click **Yes** to agree with the license agreement and continue the installation.
5. Click **Next** in the Readme File Information window.
6. Click **Next** in the Setup Progress window.
7. Setup is now complete. Click **Finish** to restart the computer and for changes to take effect.

---

## AC97 Codec Audio Driver Installation

---

Follow the steps below to install the Realtek AC97 Codec Audio Drivers.

1. Insert the CD that comes with the board. Click **Intel** at the left side, then **Intel(R) I945GM/GME/GSE Chipset Drivers**, and then **Realtek Audio Driver**.



2. Click **Realtek High Definition Codec Audio Driver**.



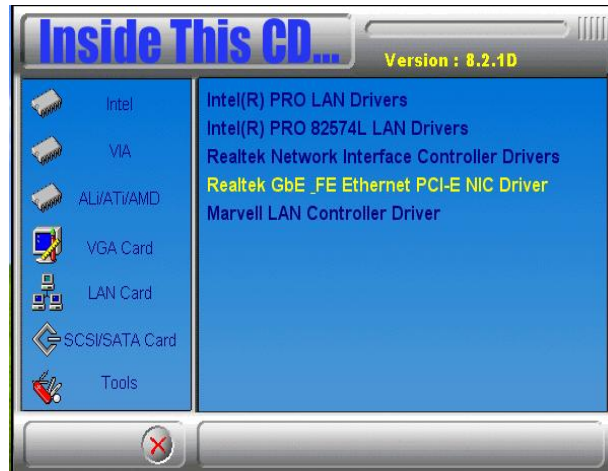
3. When the welcome screen to the InstallShield Wizard for **Realtek High Definition Codec Audio Driver** appears, click **Next** to start the installation.

4. When the InstallShieldWizard has finished performing maintenance operations on Realtek High Definition Audio Driver, click ***Finish*** to restart the computer.

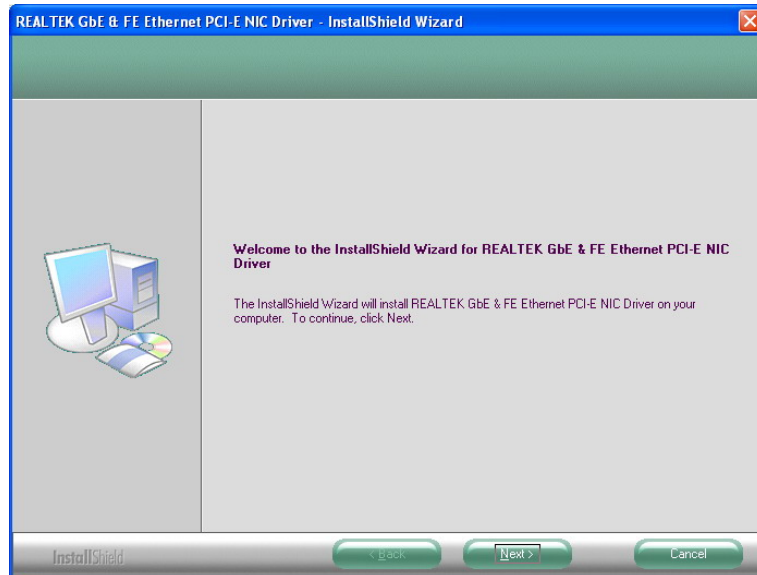


## LAN Drivers Installation

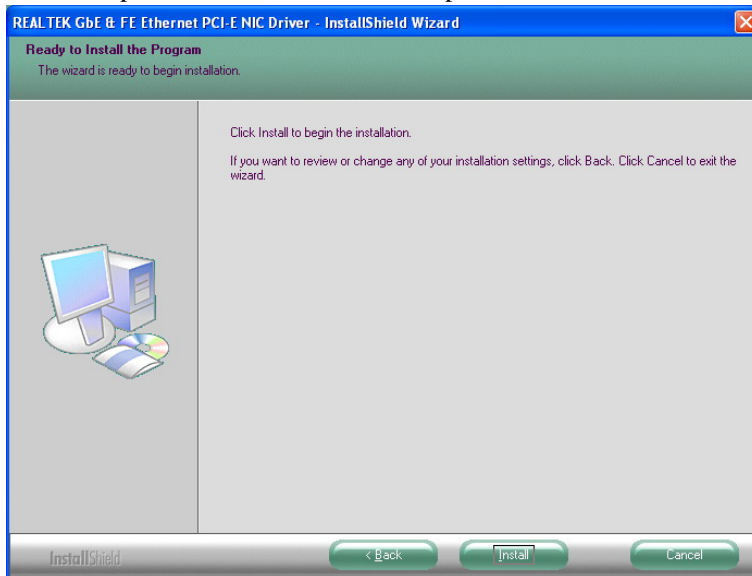
1. Insert the CD that comes with the motherboard. Click **LAN Card** on the left side and click **Realtek GbE\_FE Ethernet PCI-E NIC Driver**.



2. When the Welcome screen of the **InstallShield Wizard** appears, click **Next** to continue.



3. On the next screen, you are asked to click Install to begin the installation process. Now, click **Install** to proceed.



4. Installation is now complete, click **Finish** to exit InstallShield Wizard.

This page is intentionally left blank.

## Appendix

### A. I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses which also becomes the identity of the device. The following table lists the I/O port addresses used.

Address	Device Description
000h - 01Fh	DMA Controller #1
020h - 03Fh	Interrupt Controller #1
040h - 05Fh	Timer
060h - 06Fh	Keyboard Controller
070h - 07Fh	Real Time Clock, NMI
080h - 09Fh	DMA Page Register
0A0h - 0BFh	Interrupt Controller #2
0C0h - 0DFh	DMA Controller #2
0F0h	Clear Math Coprocessor Busy Signal
0F1h	Reset Math Coprocessor
1F0h - 1F7h	IDE Interface
278 - 27F	Parallel Port #2(LPT2)
2F8h - 2FFh	Serial Port #2(COM2)
2B0 - 2DF	Graphics adapter Controller
378h - 3FFh	Parallel Port #1(LPT1)
360 - 36F	Network Ports
3B0 - 3BF	Monochrome & Printer adapter
3C0 - 3CF	EGA adapter
3D0 - 3DF	CGA adapter
3F0h - 3F7h	Floppy Disk Controller
3F8h - 3FFh	Serial Port #1(COM1)

---

## B. Interrupt Request Lines (IRQ)

Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on board.

Level	Function
IRQ0	System Timer Output
IRQ1	Keyboard
IRQ2	Interrupt Cascade
IRQ3	Serial Port #2
IRQ4	Serial Port #1
IRQ5	Reserved
IRQ6	Floppy Disk Controller
IRQ7	Parallel Port #1
IRQ8	Real Time Clock
IRQ9	Reserved
IRQ10	Reserved
IRQ11	Reserved
IRQ12	PS/2 Mouse
IRQ13	80287
IRQ14	Primary IDE
IRQ15	Secondary IDE