MB898/MB898F

Socket LGA775 Pentium® 4
Intel® Q965 Chipset
Industrial Motherboard

USER'S MANUAL

Version 1.0

Acknowledgments

Award is a registered trademark of Award Software International, Inc.
PS/2 is a trademark of International Business Machines Corporation.
Intel and Pentium 4 are registered trademarks of Intel Corporation.
Microsoft Windows is a registered trademark of Microsoft Corporation.
Winbond is a registered trademark of Winbond Electronics Corporation.
All other product names or trademarks are properties of their respective owners.

Table of Contents

Introduction.....1

Checklist..... 1
Product Description..... 2
Specifications..... 3
Board Dimensions..... 4

Installations.....5

Installing the CPU..... 6
ATX Power Installation..... 6
Installing the Memory..... 7
Setting the Jumpers..... 8
Jumper Locations on MB898/MB898F.....9
JBAT1: Clear CMOS Contents.....10
JP1: Select the Boot Device (Factory used only).....10
JP6: IDE DMA Mode Setting.....11
JP8, JP9, JP10: RS232/422/485 (COM2) Selection.....11
JP11: Processor Setting.....11
Connectors on MB898/MB898F..... 12
ATX1: 24-pin ATX Power Connector.....15
ATX2: ATX 12V Power Connector.....15
.....15
MB898/MB898F Edge Connectors.....15
VGA1: VGA CRT Connector.....16
CN1: Parallel Port Connector.....16
CN2, J10, J6, J5: COM1/2/3/4 Serial Ports.....17
CN3: PS/2 Keyboard and PS/2 Mouse Connectors.....17
CN4: Audio Connector.....18
.....18
USB6: USB6/7 Connector.....18
.....18
USB_LAN1: 10/100 or GbE RJ-45 and USB4/5 Connector
.....18
.....18
Note: 10/100 LAN for MB898; Gigabit LAN for MB898F
.....18
F_USB1: USB0/USB1 Connector.....18
F_USB2: USB2/USB3 Connector.....19

F_USB3: USB8/USB9 Connector..... 19
S_ATA1,S_ATA2,S_ATA3,S_ATA4,S_ATA5,S_ATA6:
SATA0/1/2/3/4/5 Connector..... 19
Note: S_ATA5 and S_ATA6 MB898F only..... 19
IDE1: Primary IDE Connectors..... 20
CPU_FAN1: CPU Fan Power Connector.....20
PWR_FAN1: POWER Fan Power Connector.....20
SYS_FAN1: SYSTEM Fan Power Connector.....21
NB_FAN1: Northbridge Fan Power Connectors..... 21
J1: Audio Front Header..... 21
J2: HDMI Audio Connector..... 21
J8: System Function Connector.....22
J11: IrDA Connector..... 23
J12: Digital I/O Connector (4 in, 4 out).....24
PCIE_1: x16 PCI Express Slot.....24
PCIE_2, PCIE_3: x1 PCI Express Slots..... 24
PCI1, PCI2, PCI3, PCI4: PCI Slots.....24
J9: CF Socket.....24
Watchdog Timer Configuration..... 25

BIOS Setup.....30

BIOS Introduction..... 31
BIOS Setup..... 31
Standard CMOS Setup..... 33
Advanced BIOS Features.....36
Advanced Chipset Features..... 40
Power Management Setup.....45
PNP/PCI Configurations 47
PC Health Status 48
Frequency/Voltage Control 49
Load Fail-Safe Defaults.....50
Load Optimized Defaults.....50
Set Supervisor/User Password..... 50
Save & Exit Setup.....50
Exit Without Saving..... 50

Drivers Installation.....51

Intel Q965 Chipset Software Installation Utility..... 52
Intel Q965 Chipset Graphics Driver..... 54
Realtek Codec Audio Driver Installation..... 56
Intel LAN Drivers Installation..... 57

JMicron IDE/Compact Flash Drivers Installation.....59

Appendix.....61

A. I/O Port Address Map..... 61
B. Interrupt Request Lines (IRQ)..... 62

This page is intentionally left blank.

Introduction

Checklist

Your MB898/MB898F Pentium© 4 motherboard package should include the items listed below:

- The MB898/MB898F motherboard
- This User's manual
- 1 Back I/O shield
- 1 IDE cable
- 1 Floppy cable
- 1 SATA cable
- 1 Serial-Port cable
- 1 CD containing the following:
  - Chipset Drivers
  - Flash Memory Utility

Product Description

The MB898/MB898F LGA 775 Pentium® 4 motherboard incorporates the Intel Q965 chipset that can utilize a single LGA775 processor of up to 4.0GHz or higher and supports FSB frequency of 533/800/1066Mhz (133MHz(533MT/s), 200MHz(800MT/s) and 266MHz(1066MT/s) HCLK respectively.

The Q965 chipset is designed for use with the Pentium® 4 processor with 1M Level 2 (CPU integrated) cache. The integrated GMCH component provides the CPU interface, DDR2 interface, Hub Interface and PCI Express graphics interface.

Four DDR2 memory sockets supports DDR2 400/533/667/800 SDRAM DIMM modules of up to 8GB in capacity.

The board is designed with one Intel 82566DC PCI Express Gigabit PHY (MB898F only) or 82562V 10/100 PHY (MB898 only) LAN controllers. Six Serial ATA connectors offer 3Gb/s data throughput speed - faster than the most advanced parallel ATA.

Expansion is provided by four PCI slots, two PCI Express x1 and one PCI Express x16. Other advanced features include ten USB 2.0 ports, IrDA interface, digital I/O, four serial ports, watchdog timer and audio function. Dimensions of the board are 12” by 9.5” in an ATX form factor.

Remarks: ***MB898/MB898F supports Intel Core 2 Duo Dual Core CPU, Pentium® D Dual Core CPU and Pentium® 4 HT single core CPU. It also supports EMT64 processors.***

MB898F supports six Serial ATA connectors (RAID function), MB898 supports four Serial ATA connectors only.

Specifications

MB898/MB898F

Socket LGA775 Pentium® 4 ATX Motherboard w/ Intel® Q965 Chipset

Features

- Supports Intel Core 2 Duo / Pentium® D / Pentium® 4 HT / Celeron® D processors Up to 3.8GHz+, 533MHz/800MHz/1066MHz FSB
- DDR2 DIMM x 4, max. 8GB
- ICH8 10/100 or Intel PCI Express Gigabit Ethernet
- Integrated Intel® Q965 VGA, CRT support
- 4 x SATA II, 10 x USB 2.0, 4 x COM, Watchdog timer, Digital I/O
- 4 x PCI, 2 x PCI Express (x1), 1 x PCI Express (x16) slots

System

CPU	Socket LGA775 for Intel Core 2 Duo / Pentium® D / Pentium® 4 HT / Celeron® D, up to 3.8GHz+
System Memory	DDRII DIMM x 4, max. 8GB, 533/667/800MHz
System Chipset	Intel® Q965 + ICH8, 533MHz/800MHz/1066MHz FSB
BIOS	Award 8Mbit
Watchdog Timer	256 levels
SSD	NA
H/W Monitor	Yes
Expansion Slot	4 PCI, 2 PCI Express (x1), 1 PCI Express (x16)

Graphics

VGA Controller	Intel® Q965 integrated
VGA Memory	Shared memory Max. 224MB
LCD Interface	N/A

Ethernet

Controller	ICH8 integrated with 82562V PHY 10/100 and Intel 82566DC PCI Express Gigabit LAN
Connector	One RJ-45 on board

Multi I/O

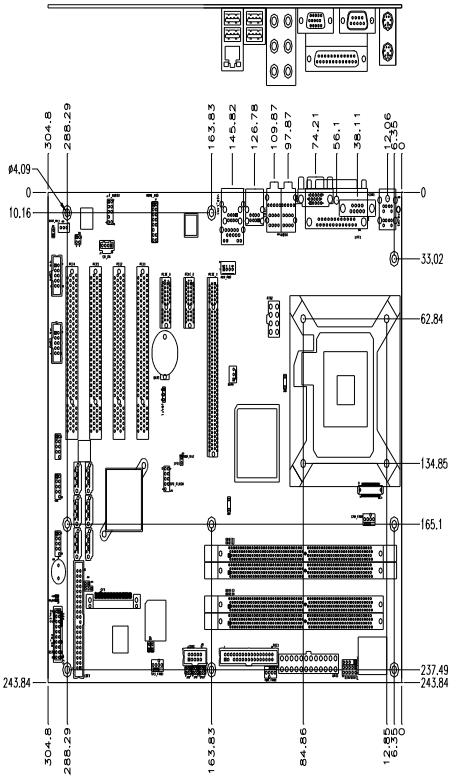
Chipset	ICH8, Winbond 83627EHF, Fintek F81216 1x IDE (UDMA33/66/100), 1x FDD, 1x KB, 1x Mouse 4x RS-232, 4x SATA II (300MB/s)
USB	4 ports on board Pin header for 6 ports (USB 2.0)
Audio	Built-in audio + AC97 codec
Others	IrDA, 4-in/4-out digital I/O

Mechanical and Environmental

Dimensions	305mm x 244mm (12“ x 9.6“)
Max. Power Requirement	TBC
Operating Temperature	0°C~60°C (32°F~140°F)
Storage Temperature	-20°C~80°C (-68°F~176°F)
Relative Humidity	10%~90% (non-condensing)

Remarks: ***MB898/MB898F supports Intel Core 2 Duo Dual Core CPU, Pentium® D Dual Core CPU and Pentium® 4 HT single core CPU. It also supports EMT64 processors.***

Board Dimensions



Installations

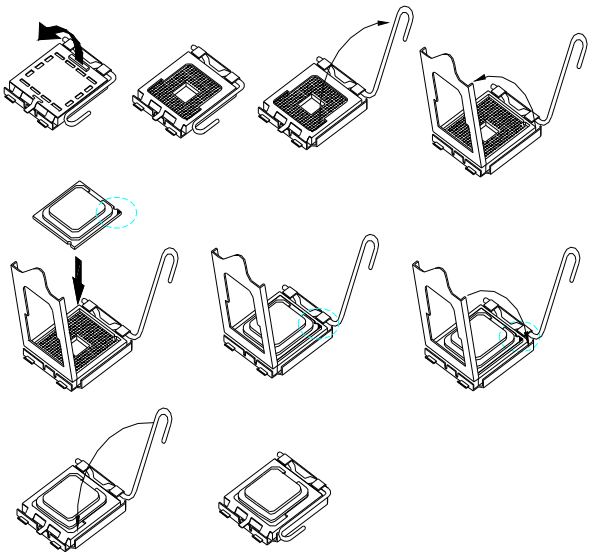
This section provides information on how to use the jumpers and connectors on the MB898/MB898F in order to set up a workable system. The topics covered are:

Installing the CPU.....
ATX Power Installation.....
Installing the Memory.....
Setting the Jumpers.....
Connectors on MB898/MB898F.....
Watchdog Timer Configuration.....

Installing the CPU

The MB898/MB898F motherboard supports an LGA 775 processor socket for Intel® Pentium® 4 processors.

The LGA 775 processor socket comes with a lever to secure the processor. Refer to the pictures below, from left to right, on how to place the processor into the CPU socket. ***Please note that the cover of the LGA775 socket must always be installed during transport to avoid damage to the socket.***



ATX Power Installation

The system power is provided to the motherboard with the ATX1 and ATX_12V power connectors. ATX1 is a 24-pin power connector and ATX_12V is a 8-pin 12V power connector.

The 24-pin power connector can to be connected to a standard 20-pin ATX power connector in a standard ATX power supply (Min. 400watt).

Note: The power supply 5VSB voltage must be at least 2A.

Installing the Memory

The MB886/MB898F motherboard supports four DDR2 memory sockets for a maximum total memory of 8GB in DDR memory type. It supports DDR2 533/667/800.

Basically, the system memory interface has the following features:

- Supports two 64-bit wide DDR data channels
- Available bandwidth up to 6.4GB/s (DDR2 800) for single-channel mode and 12.8GB/s (DDR2 800) in dual-channel mode.
- Supports 256Mb, 512Mb, 1Gb DDR2 technologies.
- Supports only x8, x16, DDR2 devices with four banks
- Supports only unbuffered DIMMs
- Supports opportunistic refresh
- Up to 32 simultaneously open pages (four per row, four rows maximum)

Dual Channel Memory Configuration

If you want to operate the Dual Channel Technology, please note the following explanations due to the limitation of Intel chipset specifications.

1. Dual Channel mode will not be enabled if only one DDR II memory module is installed.
2. To enable Dual Channel mode with two or four memory modules (it is recommended to use memory modules of identical brand, size, chips, and speed), you must install them into DIMM sockets of the same color.

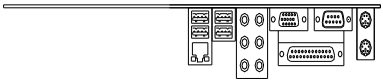
The following is a Dual Channel Memory configuration table:
(DS: Double Side, SS: Single Side)

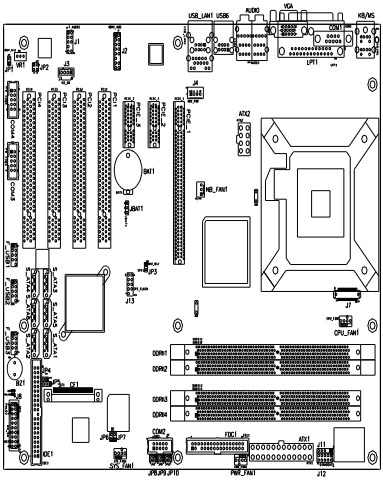
	DDRII1	DDRII2	DDRII3	DDRII4
2MemoryModules	DS/SS	X	DS/SS	X
	X	DS/SS	X	DS/SS
4MemoryModules	DS/SS	DS/SS	DS/SS	DS/SS

Setting the Jumpers

Jumpers are used on MB898/MB898F to select various settings and features according to your needs and applications. Contact your supplier if you have doubts about the best configuration for your needs. The following lists the connectors on MB898/MB898F and their respective functions.

Jumper Locations on MB898/MB898F.....
JP8, JP9, JP10: RS232/422/485 (COM2) Selection.....
JP6: IDE DMA Mode Setting.....
JBAT1: Clear CMOS Contents.....
JP1: Boot Device Selection……..……
..11
JP5: Configure and Recovery……
11



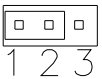
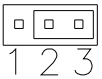


Jumper Locations on MB898/MB898F

Jumper Locations on MB898/MB898F.....
JP8, JP9, JP10: RS232/422/485 (COM2) Selection.....
JP6: IDE DMA Mode Setting.....
JBAT1: Clear CMOS Contents.....
JP1: Boot Device Selection……..……
..11
JP5: Configure and Recovery……
11

JBAT1: Clear CMOS Contents

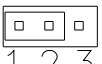
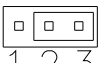
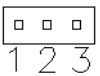
Use JBAT1, a 3-pin header, to clear the CMOS contents. *Note that the ATX-power connector should be disconnected from the motherboard before clearing CMOS.*

JBAT1	Setting	Function
	Pin 1-2 Short/Closed	Normal
	Pin 2-3 Short/Closed	Clear CMOS

JP1: Select the Boot Device (Factory used only)

JP1	Setting	Boot Device
	Pin 1-2 Short/Closed	SPI
	Pin 1-2 Open	FWH(default)

JP5: Configure and Recovery (Factory use only)

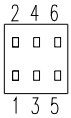
JBAT1	Setting	Function
	Pin 1-2 Short/Closed	Configure
	Pin 2-3 Short/Closed	Normal(default)
	Open	Recovery

JP6: IDE DMA Mode Setting

JP6	Setting	Function
	Pin 1-2 Short/Closed	Master (default)
	Pin 1-2 Open	Slave

JP8, JP9, JP10: RS232/422/485 (COM2) Selection

COM1 is fixed for RS-232 use only.
COM2 is selectable for RS232, RS-422 and RS-485. COM3 and COM4 are fixed for RS-232 use only. The following table describes the jumper settings for COM2 selection.



COM2 Function	RS-232	RS-422	RS-485
Jumper Setting (pin closed)	JP10: 1-2 JP9: 3-5 & 4-6 JP8: 3-5 & 4-6	JP10: 3-4 JP9: 1-3 & 2-4 JP8: 1-3 & 2-4	JP10: 5-6 JP9: 1-3 & 2-4 JP8: 1-3 & 2-4

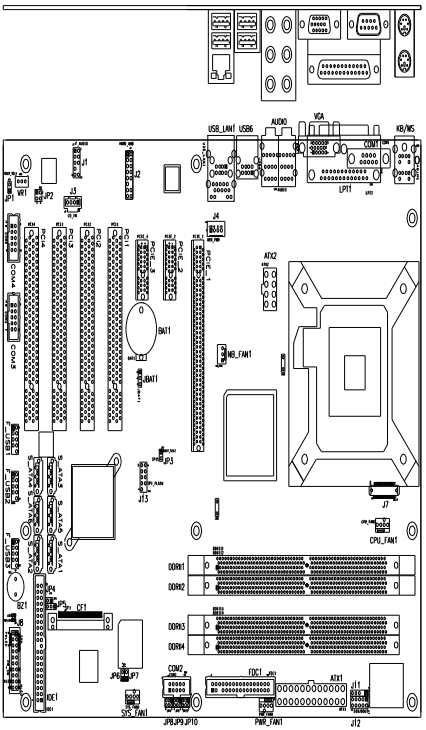
JP11: Processor Setting

JP11	Setting	Processor Used
	Pin 1-2 Short/Closed	Celeron D
	Pin 1-2 Open	Core 2 Duo, Pentium D, Pentium 4 HT

Connectors on MB898/MB898F

The connectors on MB898/MB898F allows you to connect external devices such as keyboard, floppy disk drives, hard disk drives, printers, etc. The following table lists the connectors on MB898 and their respective functions.

ATX1: 24-pin ATX Power Connector.....
ATX2: ATX 12V Power Connector.....
MB898/MB898F Edge Connectors.....
VGA1: VGA CRT Connector.....
CN1: Parallel Port Connector.....
CN2, J10, J6, J5: COM1/2/3/4 Serial Ports.....
CN3: PS/2 Keyboard and PS/2 Mouse Connectors.....
CN4: Audio Connector.....
USB6: USB6/7 Connector.....
USB_LAN1: 10/100 or GbE RJ-45 and USB4/5 Connector.....
Note: 10/100 LAN for MB898; Gigabit LAN for MB898F.....
F_USB1: USB0/USB1 Connector.....
F_USB2: USB2/USB3 Connector.....
F_USB3: USB8/USB9 Connector.....
S_ATA1,S_ATA2,S_ATA3,S_ATA4,S_ATA5,S_ATA6:
SATA0/1/2/3/4/5 Connector.....
Note: S_ATA5 and S_ATA6 MB898F only.....
IDE1: Primary IDE Connectors.....
CPU_FAN1: CPU Fan Power Connector.....
PWR_FAN1: POWER Fan Power Connector.....
SYS_FAN1: SYSTEM Fan Power Connector.....
NB_FAN1: Northbridge Fan Power Connectors.....
J1: Audio Front Header.....
J2: HDMI Audio Connector.....
J8: System Function Connector.....
J11: IrDA Connector.....
J12: Digital I/O Connector (4 in, 4 out).....
PCIE_1: x16 PCI Express Slot.....
PCIE_2, PCIE_3: x1 PCI Express Slots.....
PCI1, PCI2, PCI3, PCI4: PCI Slots.....
J9: CF Socket.....

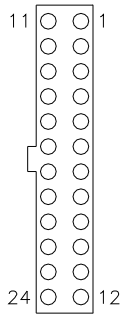


Connector Locations on MB898/MB898F

ATX1: 24-pin ATX Power Connector
ATX2: ATX 12V Power Connector
MB898/MB898F Edge Connectors
VGA1: VGA CRT Connector
CN1: Parallel Port Connector
CN2, J10, J6, J5: COM1/2/3/4 Serial Ports
CN3: PS/2 Keyboard and PS/2 Mouse Connectors
CN4: Audio Connector
USB6: USB6/7 Connector
USB_LAN1: 10/100 or GbE RJ-45 and USB4/5 Connector
Note: 10/100 LAN for MB898; Gigabit LAN for MB898F
F_USB1: USB0/USB1 Connector
F_USB2: USB2/USB3 Connector
F_USB3: USB8/USB9 Connector
S_ATA1,S_ATA2,S_ATA3,S_ATA4,S_ATA5,S_ATA6: SATA0/1/2/3/4/5 Connector
Note: S_ATA5 and S_ATA6 MB898F only
IDE1: Primary IDE Connectors
CPU_FAN1: CPU Fan Power Connector
PWR_FAN1: POWER Fan Power Connector
SYS_FAN1: SYSTEM Fan Power Connector
NB_FAN1: Northbridge Fan Power Connectors
J1: Audio Front Header
J2: HDMI Audio Connector
J8: System Function Connector
J11: IrDA Connector
J12: Digital I/O Connector (4 in, 4 out)
PCIE_1: x16 PCI Express Slot
PCIE_2, PCIE_3: x1 PCI Express Slots
PCI1, PCI2, PCI3, PCI4: PCI Slots

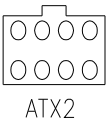
J9: CF Socket.....

ATX1: 24-pin ATX Power Connector

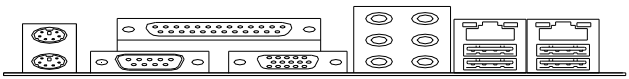


Signal Name	Pin #	Pin #	Signal Name
3.3V	13	1	3.3V
-12V	14	2	3.3V
Ground	15	3	Ground
PS-ON	16	4	+5V
Ground	17	5	Ground
Ground	18	6	+5V
Ground	19	7	Ground
-5V	20	8	Power good
+5V	21	9	5VSB
+5V	22	10	+12V
+5V	23	11	+12V
Ground	24	12	+3.3V

ATX2: ATX 12V Power Connector



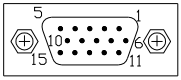
Signal Name	Pin #	Pin #	Signal Name
+12V	5	1	Ground
+12V	6	2	Ground
+12V	7	3	Ground
+12V	8	4	Ground



MB898/MB898F Edge Connectors

VGA1: VGA CRT Connector

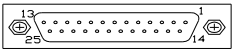
VGA1 is a DB-15 VGA connector located beside the COM1 port. The following table shows the pin-out assignments of this connector.



Signal Name	Pin #	Pin #	Signal Name
Red	1	2	Green
Blue	3	4	N.C.
GND	5	6	GND
GND	7	8	GND
VCC	9	10	GND
N.C.	11	12	DDCDATA
HSYNC	13	14	VSYNC
DDCCLK	15		

CN1: Parallel Port Connector

CN1 is a DB-25 external connector situated on top of the VGA and serial ports.

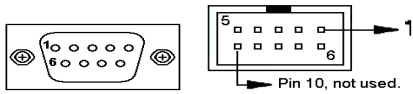


CN1 Parallel Port

Signal Name	Pin #	Pin #	Signal Name
Line printer strobe	1	14	AutoFeed
PD0, parallel data 0	2	15	Error
PD1, parallel data 1	3	16	Initialize
PD2, parallel data 2	4	17	Select
PD3, parallel data 3	5	18	Ground
PD4, parallel data 4	6	19	Ground
PD5, parallel data 5	7	20	Ground
PD6, parallel data 6	8	21	Ground
PD7, parallel data 7	9	22	Ground
ACK, acknowledge	10	23	Ground
Busy	11	24	Ground
Paper empty	12	25	Ground
Select	13	N/A	N/A

CN2, J10, J6, J5: COM1/2/3/4 Serial Ports

CN2 (COM1) is a DB-9 connector, while J10, J6 and J5 are a COM pin-header connectors.



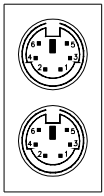
COM1 COM2

Signal Name	Pin #	Pin #	Signal Name
DCD, Data carrier detect	1	6	DSR, Data set ready
RXD, Receive data	2	7	RTS, Request to send
TXD, Transmit data	3	8	CTS, Clear to send
DTR, Data terminal ready	4	9	RI, Ring indicator
GND, ground	5	10	Not Used

COM2 is jumper selectable for RS-232, RS-422 and RS-485.

Pin #	Signal Name		
	RS-232	R2-422	RS-485
1	DCD	TX-	DATA-
2	RX	TX+	DATA+
3	TX	RX+	NC
4	DTR	RX-	NC
5	Ground	Ground	Ground
6	DSR	RTS-	NC
7	RTS	RTS+	NC
8	CTS	CTS+	NC
9	RI	CTS-	NC
10	NC	NC	NC

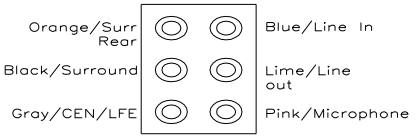
CN3: PS/2 Keyboard and PS/2 Mouse Connectors



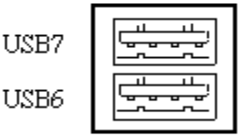
Mouse (top)
Keyboard (bottom)

Keyboard Signal	Pin #	Mouse Signal
Keyboard data	1	Mouse data
N.C.	2	N.C.
GND	3	GND
5V	4	5V
Keyboard clock	5	Mouse clock
N.C.	6	N.C.

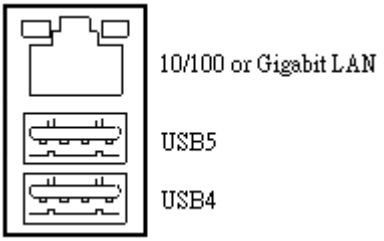
CN4: Audio Connector



USB6: USB6/7 Connector

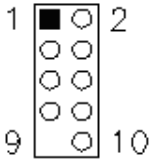


USB_LAN1: 10/100 or GbE RJ-45 and USB4/5 Connector



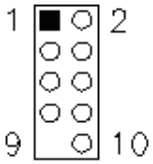
Note: 10/100 LAN for MB898; Gigabit LAN for MB898F

F_USB1: USB0/USB1 Connector



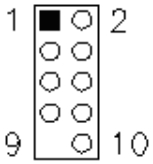
Signal Name	Pin	Pin	Signal Name
Vcc	1	2	Vcc
D0-	3	4	D1-
D0+	5	6	D1+
Ground	7	8	Ground
NC	9	10	Ground

F_USB2: USB2/USB3 Connector



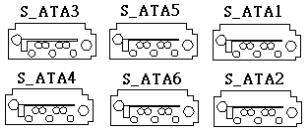
Signal Name	Pin	Pin	Signal Name
Vcc	1	2	Vcc
D2-	3	4	D3-
D2+	5	6	D3+
Ground	7	8	Ground
NC	9	10	Ground

F_USB3: USB8/USB9 Connector



Signal Name	Pin	Pin	Signal Name
Vcc	1	2	Vcc
D8-	3	4	D9-
D8+	5	6	D9+
Ground	7	8	Ground
NC	9	10	Ground

S_ATA1,S_ATA2,S_ATA3,S_ATA4,S_ATA5,S_ATA6: SATA0/1/2/3/4/5 Connector



Pin #	Signal Name
1	Ground
2	TX+
3	TX-
4	Ground
5	RX-
6	RX+
7	Ground

Note: S_ATA5 and S_ATA6 MB898F only

IDE1: Primary IDE Connectors

IDE1

Signal Name	Pin #	Pin #	Signal Name
Reset IDE	1	2	Ground
Host data 7	3	4	Host data 8
Host data 6	5	6	Host data 9
Host data 5	7	8	Host data 10
Host data 4	9	10	Host data 11
Host data 3	11	12	Host data 12
Host data 2	13	14	Host data 13
Host data 1	15	16	Host data 14
Host data 0	17	18	Host data 15
Ground	19	20	Protect pin
DRQ0	21	22	Ground
Host IOW	23	24	Ground
Host IOR	25	26	Ground
IOCHRDY	27	28	Host ALE
DACK0	29	30	Ground
IRQ14	31	32	No connect
Address 1	33	34	No connect
Address 0	35	36	Address 2
Chip select 0	37	38	Chip select 1
Activity	39	40	Ground

CPU_FAN1: CPU Fan Power Connector

Pin #	Signal Name
1	Control
2	Sense
3	+12V
4	Ground

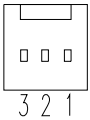
PWR_FAN1: POWER Fan Power Connector

Pin #	Signal Name
1	Control
2	Sense
3	+12V
4	Ground

SYS_FAN1: SYSTEM Fan Power Connector

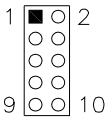
Pin #	Signal Name
1	Control
2	Sense
3	+12V
4	Ground

NB_FAN1: Northbridge Fan Power Connectors



Pin #	Signal Name
1	Sense
2	+12V
3	Rotation detection

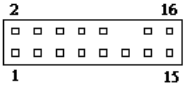
J1: Audio Front Header



Signal Name	Pin #	Pin #	Signal Name
MIC2_L	1	2	Ground
MIC2_R	3	4	Presence#
Line2_L	5	6	MIC2_ID
Sense	7	8	NC
Line2_R	9	10	Line2_ID

J2: HDMI Audio Connector

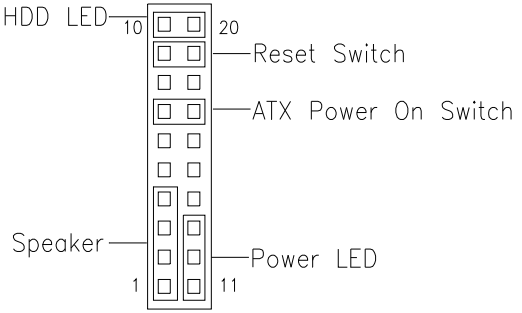
J2 is a HDMI Audio connector. The following table shows the pin-out assignments of this connector.



Signal Name	Pin #	Pin #	Signal Name
BITCLK	1	2	GND
ACZ_RST	3	4	VCC3
ACZ_SYNC	5	6	GND
SDOUT	7	8	VCC3
SDIN0	9	10	+12V
SDIN1	11	12	NC
SDIN3	13	14	3VDUAL
SDIN2	15	16	GND

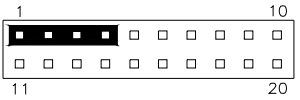
J8: System Function Connector

J8 provides connectors for system indicators that provide light indication of the computer activities and switches to change the computer status.



Speaker: Pins 1 - 4

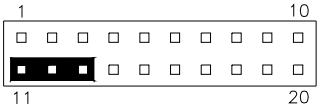
This connector provides an interface to a speaker for audio tone generation. An 8-ohm speaker is recommended.



Pin #	Signal Name
1	Speaker out
2	No connect
3	Ground
4	+5V

Power LED: Pins 11 - 13

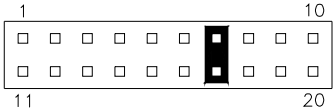
The power LED indicates the status of the main power switch.



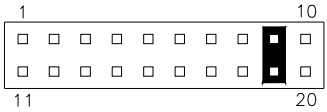
Pin #	Signal Name
11	Power LED
12	No connect
13	Ground

ATX Power ON Switch: Pins 7 and 17

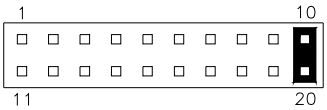
This 2-pin connector is an "ATX Power Supply On/Off Switch" on the system that connects to the power switch on the case. When pressed, the power switch will force the system to power on. When pressed again, it will force the system to power off.



Reset Switch: Pins 9 and 19
The reset switch allows the user to reset the system without turning the main power switch off and then on again. Orientation is not required when making a connection to this header.

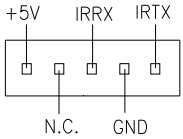


Hard Disk Drive LED Connector: Pins 10 and 20
This connector connects to the hard drive activity LED on control panel. This LED will flash when the HDD is being accessed.



Pin #	Signal Name
10	HDD Active
20	5V

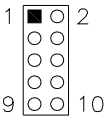
J11: IrDA Connector



Pin #	Signal Name
1	+5V
2	No connect
3	Ir RX
4	Ground
5	Ir TX

J12: Digital I/O Connector (4 in, 4 out)

This 10-pin digital I/O connector supports TTL levels and is used to control external devices requiring ON/OFF circuitry.



Signal Name	Pin #	Pin #	Signal Name
Ground	1	2	+5V
Out3	3	4	Out1
Out2	5	6	Out0
IN3	7	8	IN1
IN2	9	10	IN0

PCIE_1: x16 PCI Express Slot

PCIE_2, PCIE_3: x1 PCI Express Slots

PCI1, PCI2, PCI3, PCI4: PCI Slots

J9: CF Socket

Watchdog Timer Configuration

The WDT is used to generate a variety of output signals after a user programmable count. The WDT is suitable for use in the prevention of system lock-up, such as when software becomes trapped in a deadlock. Under these sorts of circumstances, the timer will count to zero and the selected outputs will be driven. Under normal circumstance, the user will restart the WDT at regular intervals before the timer counts to zero.

SAMPLE CODE:

```
//=====================================================================
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//=====================================================================
==
#include <stdio.h>
#include <stdlib.h>
#include "W627EHF.H"
//=====================================================================
==
int main (int argc, char *argv[]);
void copyright(void);
void EnableWDT(int);
void DisableWDT(void);
//=====================================================================
==
int main (int argc, char *argv[])
{
        unsigned char bBuf;
        unsigned char bTime;
        char **endptr;

        copyright();

        if (argc != 2)
        {
                printf(" Parameter incorrect!!\n");
                return 1;
        }

        if (Init_W627EHF() == 0)
        {
                printf(" Winbond 83627HF is not detected, program abort.\n");
                return 1;
        }
        bTime = strtol (argv[1], endptr, 10);
```

```
	printf("System will reset after %d seconds\n", bTime);

	EnableWDT(bTime);

	return 0;
}
//====================================================================
void copyright(void)
{
	printf("\n========= Winbond 83627EHF Watch Timer Tester (AUTO DETECT) ========\n"\
			"	Usage : W627E_WD reset_time\n"\
			"	Ex : W627E_WD 3  => reset system after 3 second\n"\
			"	     W627E_WD 0  => disable watch dog timer\n");
}
//====================================================================
void EnableWDT(int interval)
{
	unsigned char bBuf;

	bBuf = Get_W627EHF_Reg( 0x2D);
	bBuf &= (!0x01);
	Set_W627EHF_Reg( 0x2D, bBuf);			//Enable WDTO

	Set_W627EHF_LD( 0x08);				//switch to logic device 8
	Set_W627EHF_Reg( 0x30, 0x01);			//enable timer

	bBuf = Get_W627EHF_Reg( 0xF5);
	bBuf &= (!0x08);
	Set_W627EHF_Reg( 0xF5, bBuf);			//count mode is second

	Set_W627EHF_Reg( 0xF6, interval);		//set timer
}
//====================================================================
void DisableWDT(void)
{
	Set_W627EHF_LD(0x08);				//switch to logic device 8
	Set_W627EHF_Reg(0xF6, 0x00);			//clear watchdog timer
	Set_W627EHF_Reg(0x30, 0x00);			//watchdog disabled
}
//====================================================================
```

```
//=============================================================================
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//=============================================================================
#include "W627EHF.H"
#include <dos.h>
//=============================================================================
unsigned int W627EHF_BASE;
void Unlock_W627EHF (void);
void Lock_W627EHF (void);
//=============================================================================
unsigned int Init_W627EHF(void)
{
        unsigned int result;
        unsigned char ucDid;

        W627EHF_BASE = 0x2E;
        result = W627EHF_BASE;

        ucDid = Get_W627EHF_Reg(0x20);
        if (ucDid == 0x88)
        {       goto Init_Finish;    }

        W627EHF_BASE = 0x4E;
        result = W627EHF_BASE;
        ucDid = Get_W627EHF_Reg(0x20);
        if (ucDid == 0x88)
        {       goto Init_Finish;    }

        W627EHF_BASE = 0x00;
        result = W627EHF_BASE;

Init_Finish:
        return (result);
}
//=============================================================================
void Unlock_W627EHF (void)
{
        outportb(W627EHF_INDEX_PORT, W627EHF_UNLOCK);
        outportb(W627EHF_INDEX_PORT, W627EHF_UNLOCK);
}
//=============================================================================
void Lock_W627EHF (void)
{
        outportb(W627EHF_INDEX_PORT, W627EHF_LOCK);
}
//=============================================================================
void Set_W627EHF_LD( unsigned char LD)
{
        Unlock_W627EHF();
        outportb(W627EHF_INDEX_PORT, W627EHF_REG_LD);
        outportb(W627EHF_DATA_PORT, LD);
```

```
	Lock_W627EHF();
}
//===========================================================================
void Set_W627EHF_Reg( unsigned char REG, unsigned char DATA)
{
	Unlock_W627EHF();
	outportb(W627EHF_INDEX_PORT, REG);
	outportb(W627EHF_DATA_PORT, DATA);
	Lock_W627EHF();
}
//===========================================================================
unsigned char Get_W627EHF_Reg(unsigned char REG)
{
	unsigned char Result;
	Unlock_W627EHF();
	outportb(W627EHF_INDEX_PORT, REG);
	Result = inportb(W627EHF_DATA_PORT);
	Lock_W627EHF();
	return Result;
}
//===========================================================================




//===========================================================================
//
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//
//===========================================================================
#ifndef __W627EHF_H
#define __W627EHF_H		1
//===========================================================================
#define	W627EHF_INDEX_PORT		(W627EHF_BASE)
#define	W627EHF_DATA_PORT		(W627EHF_BASE+1)
//===========================================================================
#define	W627EHF_REG_LD			0x07
//===========================================================================
#define W627EHF_UNLOCK		0x87
#define	W627EHF_LOCK		0xAA
//===========================================================================
unsigned int Init_W627EHF(void);
void Set_W627EHF_LD( unsigned char);
void Set_W627EHF_Reg( unsigned char, unsigned char);
unsigned char Get_W627EHF_Reg( unsigned char);
//===========================================================================
#endif//__W627EHF_H
```

File of the Main.cpp

```
//===========================================================================
// THIS CODE AND INFORMATION IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY
// KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE
// IMPLIED WARRANTIES OF MERCHANTABILITY AND/OR FITNESS FOR A PARTICULAR
// PURPOSE.
//===========================================================================
#include <dos.h>
#include <conio.h>
#include <stdio.h>
#include <stdlib.h>
#include "W627HF.H"
//===========================================================================
void ClrKbBuf(void);
int main (int argc, char *argv[]);
//===========================================================================
int main (int argc, char *argv[])
{
	unsigned char ucDO = 0;				//data for digital output
	unsigned char ucDI;				//data for digital input
	unsigned char ucBuf;

	Set_W627HF_LD( 0x07);				//switch to logic device 7

	Set_W627HF_Reg(0xF1, 0x00);			//clear
	ucDI = Get_W627HF_Reg(0xF1) & 0x0F;

	ClrKbBuf();
	while(1)
	{
		ucDO++;
		Set_W627HF_Reg(0xF1, ((ucDO & 0x0F) << 4));
		ucBuf = Get_W627HF_Reg(0xF1) & 0x0F;
		if (ucBuf != ucDI)
		{
			ucDI = ucBuf;
			printf("Digital I/O Input Changed. Current Data is 0x%X\n",ucDI);
		}

		if (kbhit())
		{
			getch();
			break;
		}
		delay(500);
	}
	return 0;
}
//===========================================================================
void ClrKbBuf(void)
{
	while(kbhit())
	{	getch();	}
}
//---------------------------------------------------------------------------
```

BIOS Setup

This chapter describes the different settings available in the Award BIOS that comes with the board. The topics covered in this chapter are as follows:

BIOS Introduction.....
BIOS Setup.....
Standard CMOS Setup.....
Advanced BIOS Features.....
Advanced Chipset Features.....
Integrated Peripherals.....
Power Management Setup.....
PNP/PCI Configurations.....
PC Health Status.....
Frequency/Voltage Control.....
Load Fail-Safe Defaults.....
Load Optimized Defaults.....
Set Supervisor/User Password.....
Save & Exit Setup.....
Exit Without Saving.....

BIOS Introduction

The Award BIOS (Basic Input/Output System) installed in your computer system's ROM supports Intel processors. The BIOS provides critical low-level support for a standard device such as disk drives, serial ports and parallel ports. It also adds virus and password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

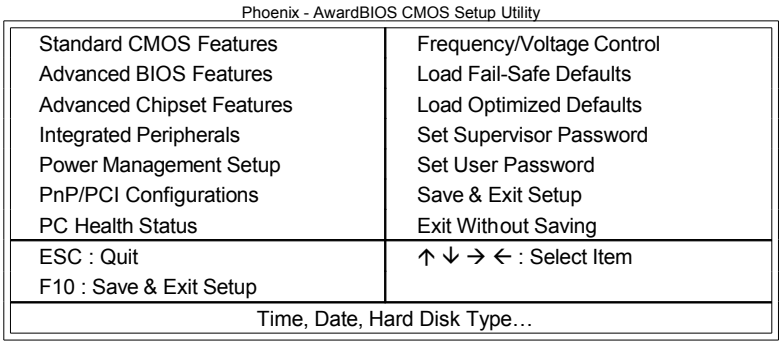
BIOS Setup

The Award BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn on the computer, the Award BIOS is immediately activated. Pressing the <Del> key immediately allows you to enter the Setup utility. If you are a little bit late pressing the <Del> key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup. If you still wish to enter Setup, restart the system by pressing the "Reset" button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system Off and back On again. The following message will appear on the screen:

```
Press  <DEL>  to  Enter  Setup
```

In general, you press the arrow keys to highlight items, <Enter> to select, the <PgUp> and <PgDn> keys to change entries, <F1> for help and <Esc> to quit.

When you enter the Setup utility, the Main Menu screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.



The section below the setup items of the Main Menu displays the control keys for this menu. At the bottom of the Main Menu just below the control keys section, there is another section, which displays information on the currently highlighted item in the list.

***Note**: If the system cannot boot after making and saving system changes with Setup, the Award BIOS supports an override to the CMOS settings that resets your system to its default.*

***Warning:** It is strongly recommended that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both Award and your system manufacturer to provide the absolute maximum performance and reliability. Changing the defaults could cause the system to become unstable and crash in some cases.*

Standard CMOS Setup

"Standard CMOS Setup" choice allows you to record some basic hardware configurations in your computer system and set the system clock and error handling. If the motherboard is already installed in a working system, you will not need to select this option. You will need to run the Standard CMOS option, however, if you change your system hardware configurations, the onboard battery fails, or the configuration stored in the CMOS memory was lost or damaged.

Phoenix - AwardBIOS CMOS Setup Utility
Standard CMOS Features

		Item Help
Date (mm:dd:yy)	Fri, Oct 20, 2006	
Time (hh:mm:ss)	16 : 11 : 00	Menu Level >
IDE Channel 0 Master	None	Change the day, month,
IDE Channel 0 Slave	None	Year and century
IDE Channel 1 Master	None	
IDE Channel 1 Slave	None	
IDE Channel 2 Master	None	
IDE Channel 3 Master	None	
Drive A	1.44M, 3.5 in.	
Drive B	None	
Video	EGA/VGA	
Halt On	All , But Keyboard	
Base Memory	640K	
Extended Memory	2086912K	
Total Memory	2087936K	

At the bottom of the menu are the control keys for use on this menu. If you need any help in each item field, you can press the <F1> key. It will display the relevant information to help you. The memory display at the lower right-hand side of the menu is read-only. It will adjust automatically according to the memory changed. The following describes each item of this menu.

Date

The date format is:

Day :	**Sun to Sat**
Month :	**1 to 12**
Date :	**1 to 31**
Year :	**1999 to 2099**

To set the date, highlight the "Date" field and use the PageUp/ PageDown or +/- keys to set the current time.

Time

The time format is: **Hour : 00 to 23**
Minute : 00 to 59
Second : 00 to 59

To set the time, highlight the "Time" field and use the <PgUp>/ <PgDn> or +/- keys to set the current time.

IDE Channel Master/Slave

MB898F with ICH8R supports 6 Serial ATA connectors, while MB898 with ICH8 supports 4 Serial ATA connectors; MB898 series boards with JMicron controller support 1 CF and 1 IDE connectors.

The onboard Serial ATA connectors provide Primary and Secondary channels for connecting up to four Serial ATA hard disks . Each channel can support up to two hard disks; the first is the "Master" and the second is the "Slave".

Press <Enter> to configure the hard disk. The selections include Auto, Manual, and None. Select 'Manual' to define the drive information manually. You will be asked to enter the following items.

Capacity :	Capacity/size of the hard disk drive
Cylinder :	Number of cylinders
Head :	Number of read/write heads
Precomp :	Write precompensation
Landing Zone :	Landing zone
Sector :	Number of sectors

The Access Mode selections are as follows:

CHS (HD < 528MB)
LBA (HD > 528MB and supports Logical Block Addressing)
Large (for MS-DOS only)
Auto

Drive A / Drive B

These fields identify the types of floppy disk drive A or drive B that has been installed in the computer. The available specifications are:

360KB	1.2MB	720KB	1.44MB	2.88MB
5.25 in.	5.25 in.	3.5 in.	3.5 in.	3.5 in.

Video

This field selects the type of video display card installed in your system. You can choose the following video display cards:

EGA/VGA	For EGA, VGA, SEGA, SVGA or PGA monitor adapters. (default)
CGA 40	Power up in 40 column mode.
CGA 80	Power up in 80 column mode.
MONO	For Hercules or MDA adapters.

Halt On

This field determines whether or not the system will halt if an error is detected during power up.

No errors	The system boot will not be halted for any error that may be detected.
All errors	Whenever the BIOS detects a non-fatal error, the system will stop and you will be prompted.
All, But Keyboard	The system boot will not be halted for a keyboard error; it will stop for all other errors
All, But Diskette	The system boot will not be halted for a disk error; it will stop for all other errors.
All, But Disk/Key	The system boot will not be halted for a keyboard or disk error; it will stop for all others.

Advanced BIOS Features

This section allows you to configure and improve your system and allows you to set up some system features according to your preference.

Phoenix - AwardBIOS CMOS Setup Utility
Advanced BIOS Features

CPU Feature	Press Enter	ITEM HELP
Hard Disk Boot Priority	Press Enter	
Virus Warning	Disabled	Menu Level >
CPU L1 and L2 Cache	Enabled	
Hyper-Threading Technology	Enabled	
Quick Power On Self Test	Enabled	
First Boot Device	Floppy	
Second Boot Device	Hard Disk	
Third Boot Device	CDROM	
Boot Other Device	Enabled	
Swap Floppy Drive	Disabled	
Boot Up Floppy Seek	Disabled	
Boot Up NumLock Status	On	
Gate A20 Option	Fast	
Typematic Rate Setting	Disabled	
Typematic Rate (Chars/Sec)	6	
Typematic Delay (Msec)	250	
Security Option	Setup	
APIC Mode	Enabled	
MPS Version Control for OS	1.4	
OS Select For DRAM>64MB	Non-OS2	
Report No FDD For WIN 95	Yes	
Small Logo (EPA) Show	Disabled	

CPU Feature

Press Enter to configure the settings relevant to CPU Feature.

Hard Disk Boot Priority

With the field, there is the option to choose, aside from the hard disks connected, "Bootable add-in Cards" which refers to other external devices.

Virus Warning

If this option is enabled, an alarm message will be displayed when trying to write on the boot sector or on the partition table on the disk, which is typical of the virus.

CPU L1 / L2 Cache

Cache memory is additional memory that is much faster than conventional DRAM (system memory). CPUs from 486-type on up contain internal cache memory, and most, but not all, modern PCs have additional (external) cache memory. When the CPU requests data, the system transfers the requested data from the main DRAM into cache memory, for even faster access by the CPU. These items allow you to enable (speed up memory access) or disable the cache function. By default, these items are Enabled.

Hyper-Threading Technology

Hyper-Threading Technology enables two logical processors on a single physical processor by replicating, partitioning, and sharing the resources within the Intel NetBurst microarchitecture pipeline.

Quick Power On Self Test

When enabled, this field speeds up the Power On Self Test (POST) after the system is turned on. If it is set to *Enabled*, BIOS will skip some items.

First/Second/Third Boot Device

These fields determine the drive that the system searches first for an operating system. The options available include *Floppy*, *LS120*, *Hard Disk*, *CDROM, ZIP100*, *USB-Floppy, USB-ZIP*, *USB-CDROM* and *Disable*.

Boot Other Device

These fields allow the system to search for an OS from other devices other than the ones selected in the First/Second/Third Boot Device.

Swap Floppy Drive

This item allows you to determine whether or not to enable Swap Floppy Drive. When enabled, the BIOS swaps floppy drive assignments so that Drive A becomes Drive B, and Drive B becomes Drive A. By default, this field is set to ***Disabled.***

Boot Up Floppy Seek

This feature controls whether the BIOS checks for a floppy drive while booting up. If it cannot detect one (either due to improper configuration or its absence), it will flash an error message.

Boot Up NumLock Status

This allows you to activate the NumLock function after you power up the system.

Gate A20 Option

This field allows you to select how Gate A20 is worked. Gate A20 is a device used to address memory above 1 MB.

Typematic Rate Setting

When disabled, continually holding down a key on your keyboard will generate only one instance. When enabled, you can set the two typematic controls listed next. By default, this field is set to ***Disabled.***

Typematic Rate (Chars/Sec)

When the typematic rate is enabled, the system registers repeated keystrokes speeds. Settings are from 6 to 30 characters per second.

Typematic Delay (Msec)

When the typematic rate is enabled, this item allows you to set the time interval for displaying the first and second characters. By default, this item is set to ***250msec.***

Security Option

This field allows you to limit access to the System and Setup. The default value is ***Setup***. When you select *System,* the system prompts for the User Password every time you boot up. When you select *Setup,* the system always boots up and prompts for the Supervisor Password only when the Setup utility is called up.

APIC Mode

APIC stands for Advanced Programmable Interrupt Controller. The default setting is ***Enabled***.

MPS Version Control for OS

This option is specifies the MPS (Multiprocessor Specification) version for the OS. MPS version 1.4 added extended configuration tables to improve support for multiple PCI bus configurations and improve future expandability. The default setting is ***1.4***.

OS Select for DRAM > 64MB

This option allows the system to access greater than 64MB of DRAM memory when used with OS/2 that depends on certain BIOS calls to access memory. The default setting is ***Non-OS/2***.

Report No FDD For WIN 95

If you are using Windows 95/98 without a floppy disk drive, select Enabled to release IRQ6. This is required to pass Windows 95/98's SCT test. You should also disable the Onboard FDC Controller in the Integrated Peripherals screen when there's no floppy drive in the system. If you set this feature to Disabled, the BIOS will not report the missing floppy drive to Win95/98.

Small Logo (EPA) Show

The EPA logo appears at the right side of the monitor screen when the system is boot up. The default setting is ***Disabled***.

Advanced Chipset Features

This Setup menu controls the configuration of the chipset.

Phoenix - AwardBIOS CMOS Setup Utility
Advanced Chipset Features

		ITEM HELP
System BIOS Cacheable	Enabled	
Memory Hole at 15M-16M	Disabled	
PCI Express Root Port Func	Press Enter	
** VGA Setting **		
PEG/On Chip VGA Control	Auto	
On-Chip Frame Buffer Size	8MB	
DVMT Mode	DVMT	
DVMT/FIXED memory Size	128MB	

System BIOS Cacheable

The setting of *Enabled* allows caching of the system BIOS ROM at F000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

Memory Hole At 15M-16M

In order to improve performance, certain space in memory can be reserved for ISA cards. This memory must be mapped into the memory space below 16 MB. The choices are *Enabled* and *Disabled*.

On-Chip VGA Setting

The fields under the On-Chip VGA Setting and their default settings are:

PEG/On Chip VGA Control: Auto
On-Chip Frame Buffer Size: 8MB
DVMT Mode: DVMT
DVMT/Fixed Memory Size: 128MB

Integrated Peripherals

This section sets configurations for your hard disk and other integrated peripherals. The first screen shows three main items for user to select. Once an item selected, a submenu appears. Details follow.

Phoenix - AwardBIOS CMOS Setup Utility
Integrated Peripherals

OnChip IDE Device	Press Enter	ITEM HELP
SuperIO Device	Press Enter	
Onboard Device	Press Enter	Menu Level >
2nd SuperIO Device	Press Enter	
USB Device Setting	Press Enter	

Phoenix - AwardBIOS CMOS Setup Utility
OnChip IDE Device

IDE HDD Block Mode	Enabled	ITEM HELP
IDE DMA transfer access	Enabled	
IDE Primary Master PIO	Auto	Menu Level >
IDE Primary Slave PIO	Auto	
IDE Primary Master UDMA	Auto	
IDE Primary Slave UDMA	Auto	
On-Chip Secondary PCI IDE	Enabled	
IDE Secondary Master PIO	Auto	
IDE Secondary Slave PIO	Auto	
IDE Secondary Master UDMA	Auto	
IDE Secondary Slave UDMA	Auto	
SATA Mode	IDE	

Phoenix - AwardBIOS CMOS Setup Utility
SuperIO Device

POWER ON Function	BUTTON ONLY	ITEM HELP
KB Power ON Password	Enter	
Hot Key power ON	Ctrl-F1	
Onboard FDC Controller	Enabled	
Onboard Serial Port 1	3F8/IRQ4	Menu Level >
Onboard Serial Port 2	2F8/IRQ3	
UART Mode Select	Normal	
RxD , TxD Active	Hi, Lo	
IR Transmission Delay	Disabled	
UR2 Duplex Mode	Half	
Use IR Pins	IR-Rx2Tx2	
Onboard Parallel Port	378/IRQ7	
Parallel Port Mode	SPP	
EPP Mode Select	EPP1.7	
ECP Mode Use DMA	3	

Phoenix - AwardBIOS CMOS Setup Utility
Onboard Device

Intel 82562V LAN Control	Enabled	ITEM HELP
		Menu Level >

Phoenix - AwardBIOS CMOS Setup Utility

2nd Super IO Device

Onboard Serial Port 3	3E8h	ITEM HELP
Serial Port 3 Use IRQ	IRQ11	Menu Level >
Onboard Serial Port 4	2E8h	
Serial Port 4 Use IRQ	IRQ10	

Phoenix - AwardBIOS CMOS Setup Utility

USB Device Setting

USB 1.0 Controller	Enabled	ITEM HELP
USB 2.0 Controller	Enabled	Menu Level >
USB Keyboard Function	Enabled	
USB Mouse Function	Enabled	
USB Storage Function	Enabled	
*** USB Mass Storage Device Boot Setting ***		

IDE HDD Block Mode

This field allows your hard disk controller to use the fast block mode to transfer data to and from your hard disk drive.

IDE DMA Transfer Access

This field, by default, is Enabled

OnChip Secondary PCI IDE

This field, by default, is Enabled

IDE Primary/Secondary Master/Slave PIO

These fields allow your system hard disk controller to work faster. Rather than have the BIOS issue a series of commands that transfer to or from the disk drive, PIO (Programmed Input/Output) allows the BIOS to communicate with the controller and CPU directly.

The system supports five modes, numbered from 0 (default) to 4, which primarily differ in timing. When Auto is selected, the BIOS will select the best available mode.

IDE Primary/Secondary Master/Slave UDMA

These fields allow your system to improve disk I/O throughput to 33Mb/sec with the Ultra DMA/33 feature. The options are *Auto* and *Disabled*.

SATA Mode

The setting choices for the SATA Mode are IDE, RAID and AHCI Mode. Select [IDE] if you want to have SATA function as IDE. Select [AHCI] for Advanced Host Controller Interface (AHCI) feature, with improved SATA performance with native command queuing & native hot plug. Select [RAID] to use SATA as RAID function. RAID function is supported on the board if it uses ICH8R.

Power ON Function

This field is related to how the system is powered on – such as with the use of conventional power button, keyboard or hot keys. The default is ***BUTTON ONLY***.

KB Power ON Password

This field allows users to set the password when keyboard power on is the mode of the Power ON function.

Hot Key Power ON

This field sets certain keys, also known as hot keys, on the keyboard that can be used as a 'switch' to power on the system.

Onboard FDC Controller

Select *Enabled* if your system has a floppy disk controller (FDC) installed on the motherboard and you wish to use it. If you install an add-in FDC or the system has no floppy drive, select Disabled in this field. This option allows you to select the onboard FDD port.

Onboard Serial/Parallel Port

These fields allow you to select the onboard serial and parallel ports and their addresses. The default values for these ports are:

Serial Port 1	3F8/IRQ4
Serial Port 2	2F8/IRQ3
Serial Port 3	3E8/IRQ11
Serial Port 4	2E8/IRQ10
Parallel Port	378H/IRQ7

UART Mode Select

This field determines the UART 2 mode in your computer. The default value is ***Normal***. Other options include *IrDA* and *ASKIR*.

Parallel Port Mode

This field allows you to determine parallel port mode function.

SPP	Standard Printer Port
EPP	Enhanced Parallel Port
ECP	Extended Capabilities Port
ECP+EPP	Combination of ECP and EPP capabilities
Normal	Normal function

Intel 82562V LAN Control (MB898)

By default, this setting is Enabled.

USB 1.0 Controller

The options for this field are *Enabled* and *Disabled*. By default, this field is set to ***Enabled***.

USB 2.0 Controller

The options for this field are *Enabled* and *Disabled*. By default, this field is set to ***Enabled***. In order to use USB 2.0, necessary OS drivers must be installed first. ***Please update your system to Windows 2000 SP4 or Windows XP SP2.***

USB Keyboard/Mouse/Storage Function

The options for this field are *Enabled* and *Disabled*. By default, this field is set to ***Disabled***.

Power Management Setup

Phoenix - AwardBIOS CMOS Setup Utility
Power Management Setup

ACPI Function	Enabled	ITEM HELP
ACPI Suspend	S1(POS)	
Power Management	User Define	Menu Level >
Video Off Method	V/H SYNC+Blank	
Video Off In Suspend	Yes	
Suspend Type	Stop Grant	
Modem Use IRQ	3	
Suspend Mode	Disabled	
HDD Power Down	Disabled	
Soft-Off by PWR-BTTN	Instant-Off	
Resume by Alarm	Disabled	
Date (of Month) Alarm	0	
Time (hh:mm:ss) Alarm	0 : 0 : 0	
** Reload Global Timer Events **		
Primary IDE 0	Enabled	
Primary IDE 1	Enabled	
Secondary IDE 0	Enabled	
Secondary IDE 1	Enabled	
FDD, COM, LPT Port	Enabled	
PCI PIRQ[A-D] #	Enabled	

ACPI Function

Enable this function to support ACPI (Advance Configuration and Power Interface).

ACPI Suspend

The default setting of the ACPI Suspend mode is ***S1(POS)***.

Power Management

This field allows you to select the type of power saving management modes. There are four selections for Power Management.

Min. Power Saving	Minimum power management
Max. Power Saving	Maximum power management.
User Define	Each of the ranges is from 1 min. to 1hr. Except for HDD Power Down which ranges from 1 min. to 15 min.

Video Off Method

This field defines the Video Off features. There are three options.

V/H SYNC + Blank	Default setting, blank the screen and turn off vertical and horizontal scanning.
DPMS	Allows BIOS to control the video display.
Blank Screen	Writes blanks to the video buffer.

Video Off In Suspend

When enabled, the video is off in suspend mode.

Suspend Type

The default setting for the Suspend Type field is ***Stop Grant***.

Modem Use IRQ

This field sets the IRQ used by the Modem. By default, the setting is ***3***.

Suspend Mode

When enabled, and after the set time of system inactivity, all devices except the CPU will be shut off.

HDD Power Down

When enabled, and after the set time of system inactivity, the hard disk drive will be powered down while all other devices remain active.

Soft-Off by PWRBTN

This field defines the power-off mode when using an ATX power supply. The *Instant Off* mode allows powering off immediately upon pressing the power button. In the *Delay 4 Sec* mode, the system powers off when the power button is pressed for more than four seconds or enters the suspend mode when pressed for less than 4 seconds.

Resume by Alarm

This field enables or disables the resumption of the system operation. When enabled, the user is allowed to set the *Date* and *Time*.

Reload Global Timer Events

The HDD, FDD, COM, LPT Ports, and PCI PIRQ are I/O events that can prevent the system from entering a power saving mode or can awaken the system from such a mode. When an I/O device wants to gain the attention of the operating system, it signals this by causing an IRQ to occur. When the operating system is ready to respond to the request, it interrupts itself and performs the service.

PNP/PCI Configurations

This option configures the PCI bus system. All PCI bus systems on the system use INT#, thus all installed PCI cards must be set to this value.

Phoenix - AwardBIOS CMOS Setup Utility
PnP/PCI Configurations

		ITEM HELP
Init Display First	PCI Slot	
Reset Configuration Data	Disabled	Menu Level
Resources Controlled By	Auto (ESCD)	Select Yes if you are using a Plug and Play capable operating system Select No if you need the BIOS to configure non-boot devices
IRQ Resources	Press Enter	
PCI/VGA Palette Snoop	Disabled	
INT Pin 1 Assignment	Auto	
INT Pin 2 Assignment	Auto	
INT Pin 3 Assignment	Auto	
INT Pin 4 Assignment	Auto	
INT Pin 5 Assignment	Auto	
INT Pin 6 Assignment	Auto	
INT Pin 7 Assignment	Auto	
INT Pin 8 Assignment	Auto	
PCI Express relative items		
Maximum Payload Size	128	

Init Display First

The default setting is ***PCI Card***.

Reset Configuration Data

The default value is ***Disabled.***

Resources Controlled by

This PnP BIOS can configure all of the boot and compatible devices with the use of a PnP operating system such as Windows 95.

PCI/VGA Palette Snoop

Some non-standard VGA display cards may not show colors properly. This field allows you to set whether or not MPEG ISA/VESA VGA cards can work with PCI/VGA. When this field is enabled, a PCI/VGA can work with an MPEG ISA/VESA VGA card. When this field is disabled, a PCI/VGA cannot work with an MPEG ISA/VESA card.

Maximum Payload Size

The default setting of the PCI Express Maximum Payload Size is 128.

PC Health Status

Phoenix - AwardBIOS CMOS Setup Utility
PC Health Status

Shutdown Temperature	Disabled	ITEM HELP
CPU Warning Temperature	Disabled	
System Temp	32°C/89°F	
CPU TEMP	39°C/102°F	Menu Level >
System Fan Speed	0 RPM	
CPU Fan Speed	4500 RPM	
Power Fan Speed	0 RPM	
Vcore	1.26 V	
12 V	12.45 V	
1.8 V	1.90 V	
5 V	5.22 V	
3.3 V	3.36V	
VBAT (V)	3.18 V	
5VSB(V)	5.61 V	

CPU Warning Temperature

This field allows the user to set the temperature so that when the temperature is reached, the system sounds a warning. This function can help prevent damage to the system that is caused by overheating.

Shutdown Temperature

This field allows the user to set the temperature by which the system automatically shuts down once the threshold temperature is reached. This function can help prevent damage to the system that is caused by overheating.

Temperatures/Fan Speeds/Voltages

These fields are the parameters of the hardware monitoring function feature of the board. The values are read-only values as monitored by the system and show the PC health status.

Frequency/Voltage Control

Phoenix - AwardBIOS CMOS Setup Utility
Frequency/Voltage Control

Auto Detect PCI Clk	Disabled	ITEM HELP
Spread Spectrum	Disabled	Menu Level >

Auto Detect PCI Clk

This field enables or disables the auto detection of the PCI clock.

Spread Spectrum

This field sets the value of the spread spectrum. The default setting is ***Disabled***. This field is for CE testing use only.

Load Fail-Safe Defaults

This option allows you to load the troubleshooting default values permanently stored in the BIOS ROM. These default settings are non-optimal and disable all high-performance features.

Load Optimized Defaults

This option allows you to load the default values to your system configuration. These default settings are optimal and enable all high performance features.

Set Supervisor/User Password

These two options set the system password. Supervisor Password sets a password that will be used to protect the system and Setup utility. User Password sets a password that will be used exclusively on the system. To specify a password, highlight the type you want and press <Enter>. The Enter Password: message prompts on the screen. Type the password, up to eight characters in length, and press <Enter>. The system confirms your password by asking you to type it again. After setting a password, the screen automatically returns to the main screen.

To disable a password, just press the <Enter> key when you are prompted to enter the password. A message will confirm the password to be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

Save & Exit Setup

This option allows you to determine whether or not to accept the modifications. If you type "Y", you will quit the setup utility and save all changes into the CMOS memory. If you type "N", you will return to Setup utility.

Exit Without Saving

Select this option to exit the Setup utility without saving the changes you have made in this session. Typing "Y" will quit the Setup utility without saving the modifications. Typing "N" will return you to Setup utility.

Drivers Installation

This section describes the installation procedures for software and drivers under the Windows 2000 and Windows XP. The software and drivers are included with the board. If you find the items missing, please contact the vendor where you made the purchase. The contents of this section include the following:

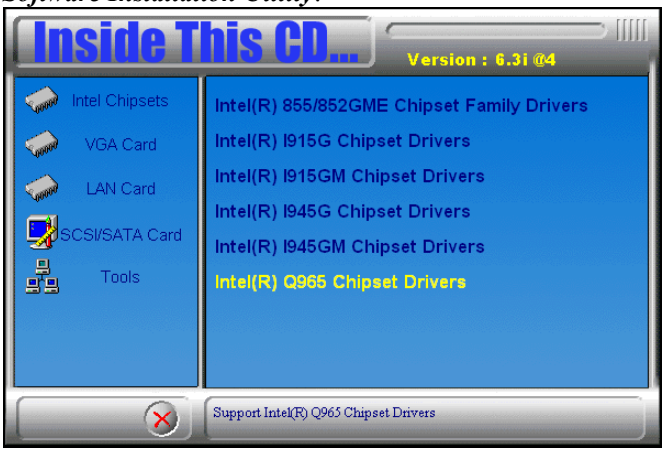
Intel Q965 Chipset Software Installation Utility.....
Intel Q965 Chipset Graphics Driver.....
Realtek Codec Audio Driver Installation.....
Intel LAN Drivers Installation.....
JMicron IDE/Compact Flash Drivers Installation.....

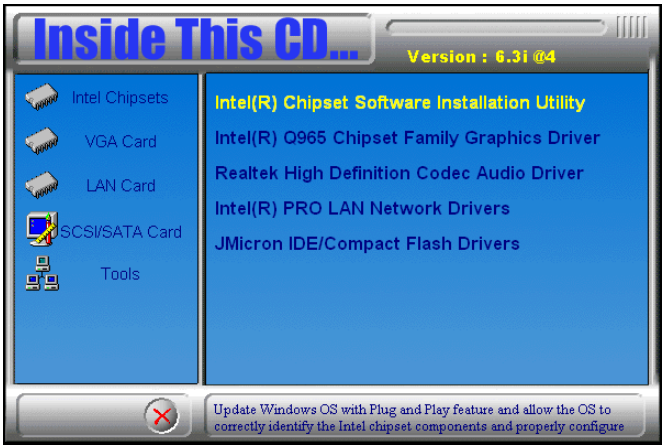
IMPORTANT NOTE:
After installing your Windows operating system (Windows 2000/XP), you must install first the Intel Chipset Software Installation Utility before proceeding with the drivers installation.

Intel Q965 Chipset Software Installation Utility

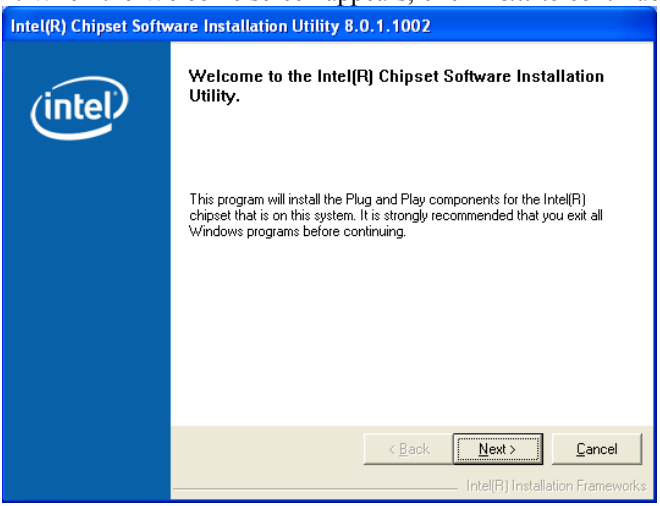
The Intel® Q965 Chipset Drivers should be installed first before the software drivers to enable Plug & Play INF support for Intel chipset components. Follow the instructions below to complete the installation under Windows 2000/XP. (Before installed Intel Chipset Software Installation Utility,Please update your system to Windows 2000 SP4 or Windows XP SP1A)

1. Insert the CD that comes with the board and the screen below would appear. Click ***Intel (R) Q965 Chipset Drivers***, then ***Intel(R) Chipset Software Installation Utility***.





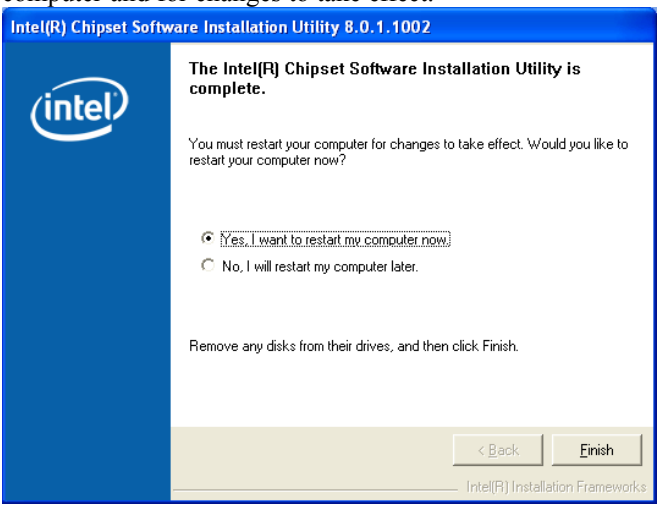
2. When the Welcome screen appears, click ***Next*** to continue.



3. Click ***Yes*** to accept the software license agreement and proceed with the installation process.

4. On the Readme Information screen, click ***Next*** to continue the installation.

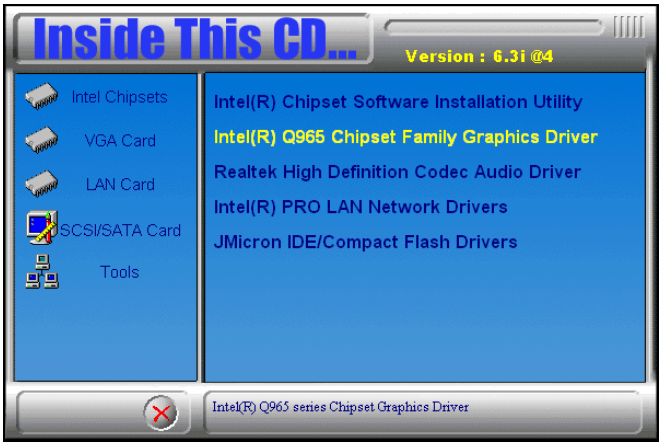
5. The Setup process is now complete. Click ***Finish*** to restart the computer and for changes to take effect.



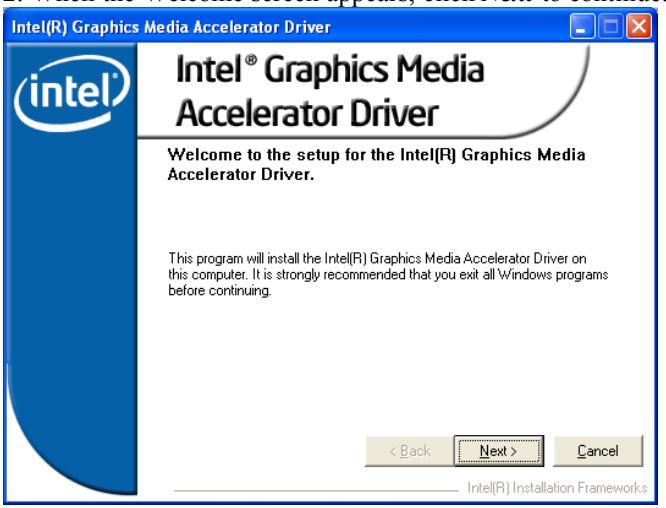
Intel Q965 Chipset Graphics Driver

Follow the instructions below to complete the installation under Windows 2000/XP.

1. Insert the CD that comes with the board and the screen below would appear. Click ***Intel (R) Q965 Chipset Drivers***, then ***Intel (R) Q965 Chipset Family Graphics Driver***.

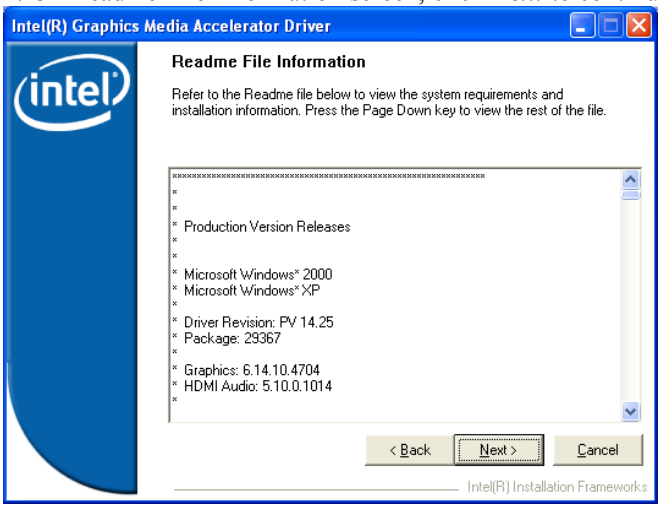


2. When the Welcome screen appears, click ***Next*** to continue.

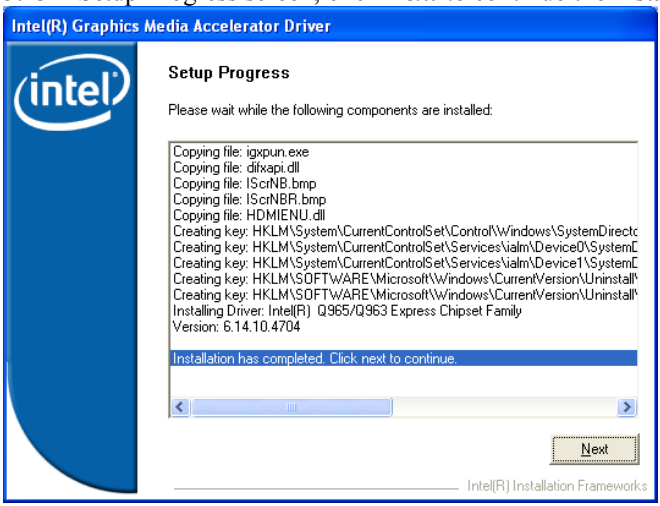


3. Click ***Yes*** to accept the software license agreement and proceed with the installation process.

4. On Readme File Information screen, click ***Next*** to continue.



5. On Setup Progress screen, click ***Next*** to continue the installation.

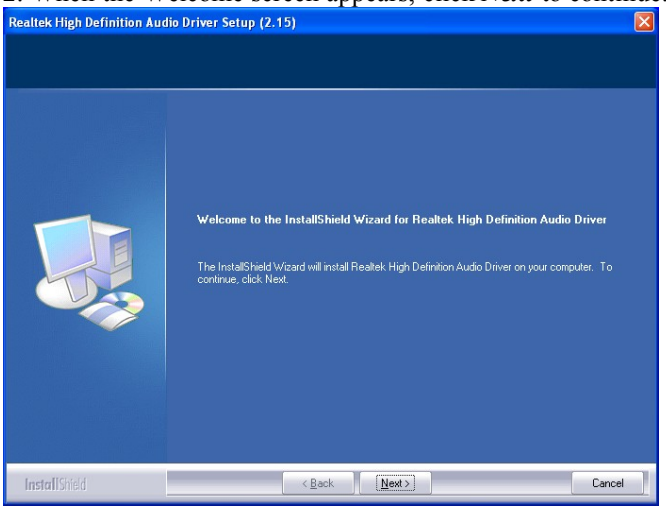


6. The Setup process is now complete. Click ***Finish*** to restart the computer and for changes to take effect.

Realtek Codec Audio Driver Installation

1. Insert the CD that comes with the board and the screen below would appear. Click ***Intel (R) Q965 Chipset Drivers***, then ***Realtek High Definition Codec Audio Driver***.

2. When the Welcome screen appears, click ***Next*** to continue.

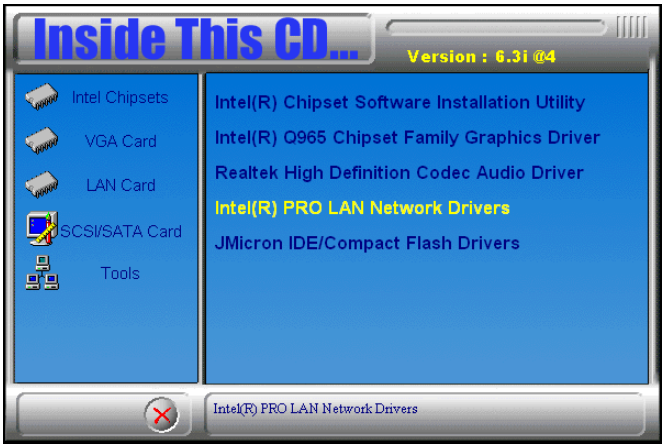


3. The Setup process is now complete. Restart the computer when prompted for changes to take effect.

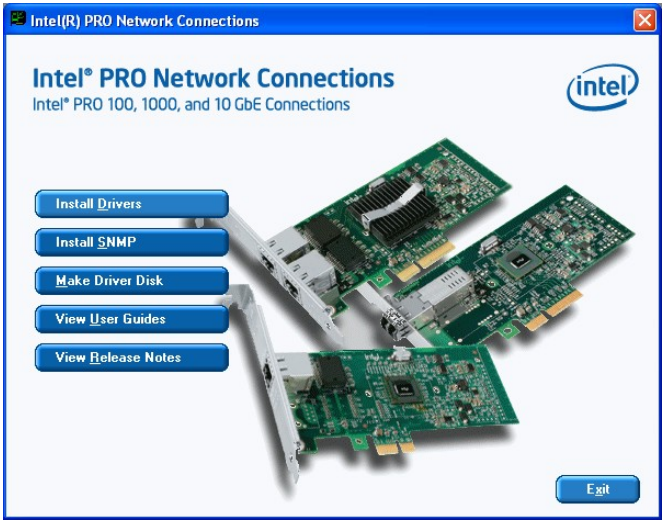
Intel LAN Drivers Installation

Follow the steps below to start installing the Intel PCI Express Gigabit LAN drivers.

1. Insert the CD that comes with the board. On the initial screen, Click ***Intel (R) Q965 Chipset Drivers,*** then ***Intel(R) PRO LAN Network Drivers***.



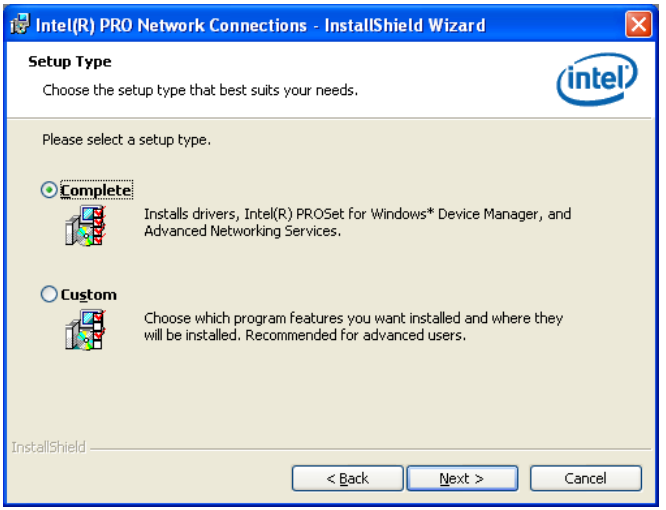
2. On the next screen, click ***Install Drivers*** to start the drivers installation.



3. When the Welcome screen appears, click ***Next*** to continue.

4. In the License Agreement screen, click ***I accept the terms in license agreement*** and ***Next*** to accept the software license agreement and proceed with the installation process.

5. When the Setup Type appears, click ***Complete*** and ***Next*** to continue.

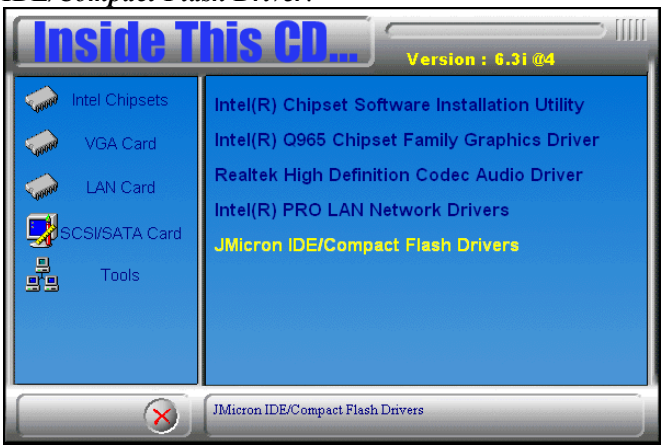


6. When the Ready to Install the Program screen appears, click ***Install*** to continue.

7. The Setup process is now complete (InstallShield Wizard Completed). Click ***Finish*** to restart the computer and for changes to take effect.

JMicron IDE/Compact Flash Drivers Installation

1. Insert the CD that comes with the board and the screen below would appear. Click ***Intel (R) Q965 Chipset Drivers***, then ***JMicron IDE/Compact Flash Driver***.



2. When the Welcome screen appears (Welcome to the InstallShield Wizard for JRAID), click ***Next*** to continue.

3. When the Ready to Install the Program screen appears (The wizard is ready to begin installation), click ***Install*** to continue.

4. The Setup process is now complete (InstallShield Wizard Complete screen). Click ***Finish*** to restart the computer and for changes to take effect.

This page is intentionally left blank.

Appendix

A. I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses that also becomes the identity of the device. The following table lists the I/O port addresses used.

Address	Device Description
000h - 01Fh	DMA Controller #1
020h - 03Fh	Interrupt Controller #1
040h - 05Fh	Timer
060h - 06Fh	Keyboard Controller
070h - 07Fh	Real Time Clock, NMI
080h - 09Fh	DMA Page Register
0A0h - 0BFh	Interrupt Controller #2
0C0h - 0DFh	DMA Controller #2
0F0h	Clear Math Coprocessor Busy Signal
0F1h	Reset Math Coprocessor
1F0h - 1F7h	IDE Interface
278h - 27Fh	Parallel Port #2(LPT2)
2F8h - 2FFh	Serial Port #2(COM2)
2B0h - 2DFh	Graphics adapter Controller
378h - 3FFh	Parallel Port #1(LPT1)
360h - 36Fh	Network Ports
3B0h - 3BFh	Monochrome & Printer adapter
3C0h - 3CFh	EGA adapter
3D0h - 3DFh	CGA adapter
3F0h - 3F7h	Floppy Disk Controller
3F8h - 3FFh	Serial Port #1(COM1)

B. Interrupt Request Lines (IRQ)

Peripheral devices use interrupt request lines to notify CPU for the service required. The following table shows the IRQ used by the devices on board.

Level	Function
IRQ0	System Timer Output
IRQ1	Keyboard
IRQ2	Interrupt Cascade
IRQ3	Serial Port #2
IRQ4	Serial Port #1
IRQ5	Reserved
IRQ6	Floppy Disk Controller
IRQ7	Parallel Port #1
IRQ8	Real Time Clock
IRQ9	Reserved
IRQ10	Reserved
IRQ11	Reserved
IRQ12	PS/2 Mouse
IRQ13	80287
IRQ14	Primary IDE
IRQ15	Secondary IDE